

Product Highlight

Features

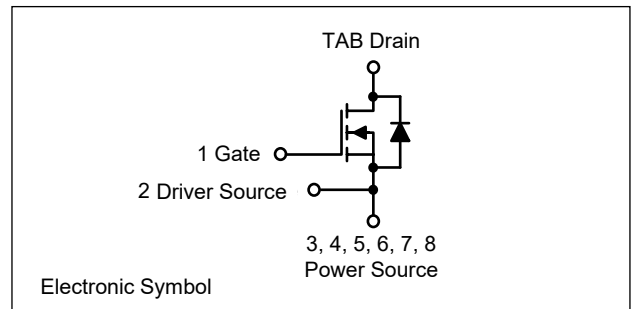
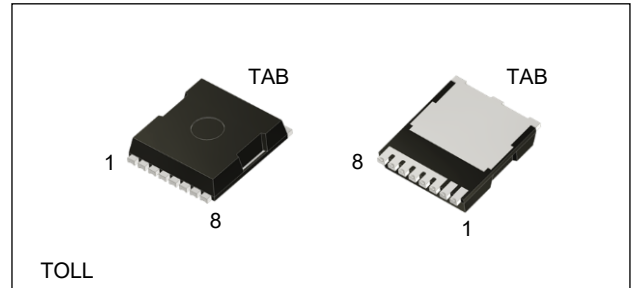
- High switching speed with a low gate charge
- Fast intrinsic diode with low reverse recovery
- Robust Avalanche Capability
- 100% Avalanche Tested
- Pb-free, Halogen Free, and RoHS Compliant

Benefits

- System efficiency improvement
- Higher frequency applicability
- Increased power density
- Reduced cooling effort

Applications

- Server & Telecom power
- EV charging station
- Solar inverter / ESS / UPS
- Industrial power supply



Key Parameters

$BV_{DSS, T_C=25^\circ C}$	$I_D, T_C=25^\circ C$	$R_{DS(on), typ}$	$Q_{g, typ}$
650 V	51 A	45 mΩ	55 nC

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
MSTB065R045T2RH	065R045T2	TOLL	Tape & Reel	1000 units

Absolute Maximum Ratings ($T_C = 25^\circ C$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		650	V
V_{GS}	Gate to Source Voltage (DC)		-10 / +22	V
V_{GSop}	Recommended Operation Value		-5 / +18	V
I_D	Drain Current	Continuous ($T_C = 25^\circ C$)	51*	A
		Continuous ($T_C = 100^\circ C$)	36*	
I_{DM}	Drain Current	Pulsed (Note1)	137*	A
P_D	Power Dissipation	($T_C = 25^\circ C$)	214	W
		Derate Above $25^\circ C$	1.43	W/ $^\circ C$
T_J	Operating Temperature Range		-55 to 175	$^\circ C$

*Limited by maximum junction temperature.

Note 1. Repetitive rating: pulse-width limited by maximum junction temperature.

1. Package

Temperature Ratings

Symbol	Parameter	Value	Unit
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds	260	°C

Thermal Characteristics

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance, Junction to Case, Max.	0.7	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	40	

2. MOSFET

Electrical Characteristics (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA	650			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 650 V, V _{GS} = 0 V		1	100	μA
		V _{DS} = 650 V, V _{GS} = 0 V, T _J = 175°C		10		
I _{GSS}	Gate-Source Leakage Current	V _{GS} = +22 V, V _{DS} = 0 V			+100	nA
		V _{GS} = -10 V, V _{DS} = 0 V			-100	
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 7.0 mA (tested after V _{GS} = 22V, 1 ms pulse)	1.8	2.8	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 18 V, I _D = 20 A		45	63	mΩ
		V _{GS} = 18 V, I _D = 20 A, T _J = 175°C		61		
g _{fs}	Transconductance	V _{DS} = 20 V, I _D = 20 A		13.3		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 400 V, V _{GS} = 0V, f = 250 kHz		1238		pF
C _{oss}	Output Capacitance			137		
C _{rss}	Reverse Capacitance			7.7		
E _{oss}	Stored Energy in Output Capacitance	V _{DS} = 0 V to 400 V, V _{GS} = 0 V		13.6		μJ
C _{o(er)}	Energy Related Output Capacitance			169		pF
C _{o(tr)}	Time Related Output Capacitance			242		
Q _{g(tot)}	Total Gate Charge	V _{DS} = 400 V, I _D = 20 A, V _{GS} = -5 V / 18 V, Inductive load		55		nC
Q _{gs}	Gate to Source Charge			16		
Q _{gd}	Gate to Drain “Miller” Charge			14		
R _G	Internal Gate Resistance	f = 1MHz		2.5		Ω
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} = 400 V, I _D = 20 A, V _{GS} = -5 V / 18 V, R _G = 5.6 Ω, Inductive load		13.9		ns
t _r	Turn-On Rise Time			10.8		
t _{d(off)}	Turn-Off Delay Time			27.9		
t _f	Turn-Off Fall Time			6.0		
E _{on}	Turn-on Switching Energy			33.6		μJ
E _{off}	Turn-off Switching Energy			22.8		
E _{tot}	Total Switching Energy			56.4		

3. Body Diode

Electrical Characteristics (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Source-Drain Diode Characteristics						
I _S	Maximum Continuous Diode Forward Current				51*	A
I _{SM}	Maximum Pulsed Diode Forward Current				137*	
V _{SD}	Diode Forward Voltage	V _{GS} = -5 V, I _{SD} = 20 A		4.4		V
t _{rr}	Reverse Recovery Time	V _{DD} = 400 V, I _{SD} = 20 A, dI _F /dt = 1000 A/μs, Includes Q _{OSS}		17.9		ns
Q _{rr}	Reverse Recovery Charge			107		nC
I _{rrm}	Peak Reverse Recovery Current			10.1		A

*Limited by maximum junction temperature.

4. Typical Performance Characteristics

Figure 1. On-Region Characteristics $T_J = -40^{\circ}\text{C}$

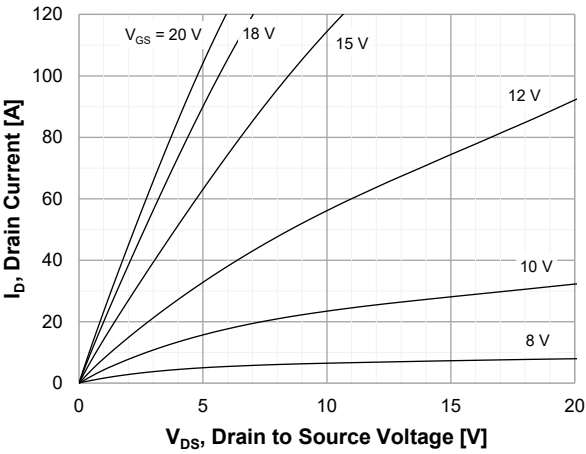


Figure 2. On-Region Characteristics $T_J = 25^{\circ}\text{C}$

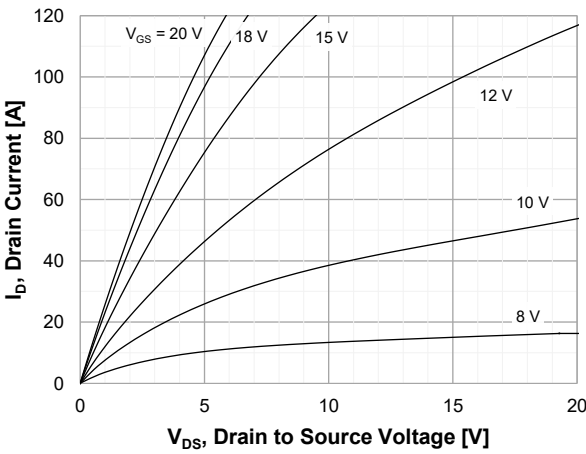


Figure 3. On-Region Characteristics $T_J = 125^{\circ}\text{C}$

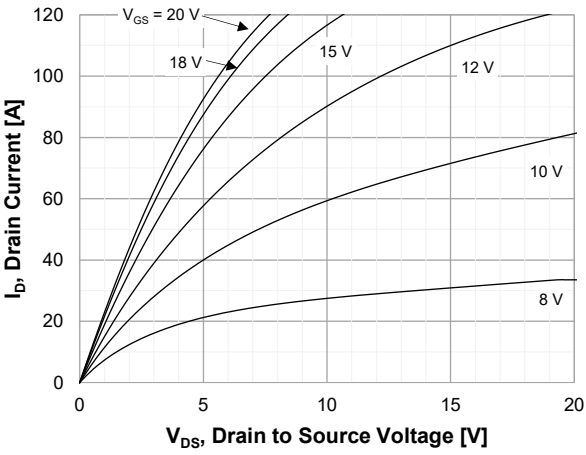


Figure 4. On-Region Characteristics $T_J = 175^{\circ}\text{C}$

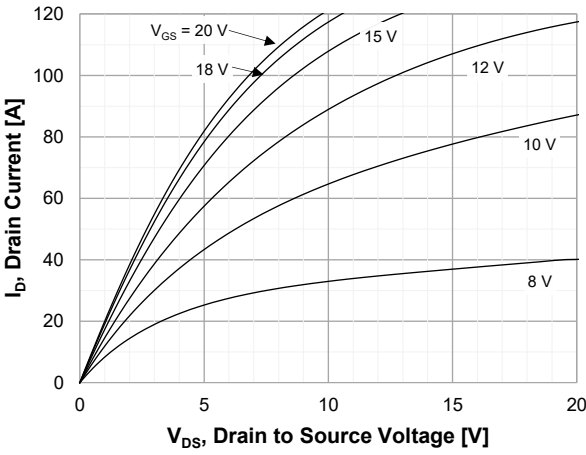


Figure 5. On-Resistance Characteristics vs. Temperature

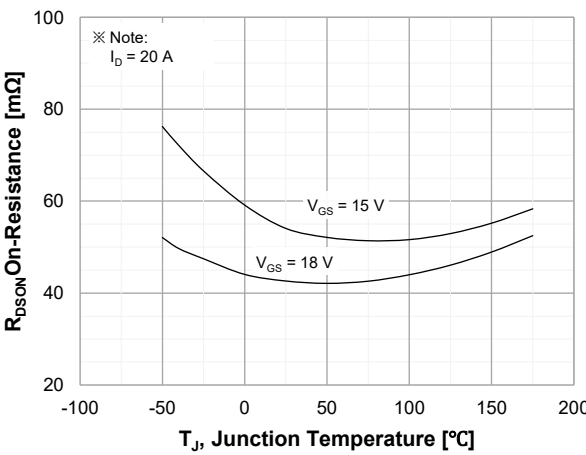
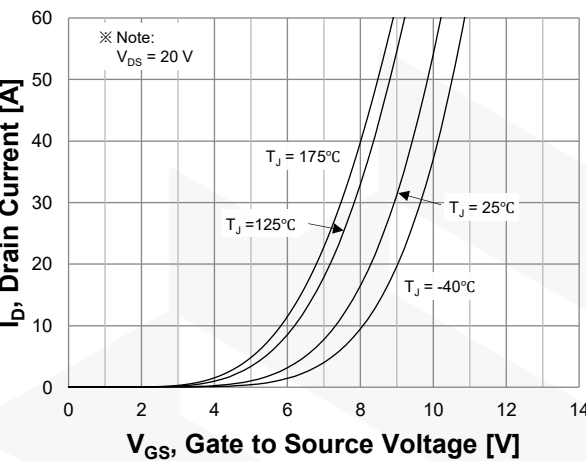


Figure 6. Transfer Characteristics



4. Typical Performance Characteristics

Figure 7. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = -40^{\circ}\text{C}$

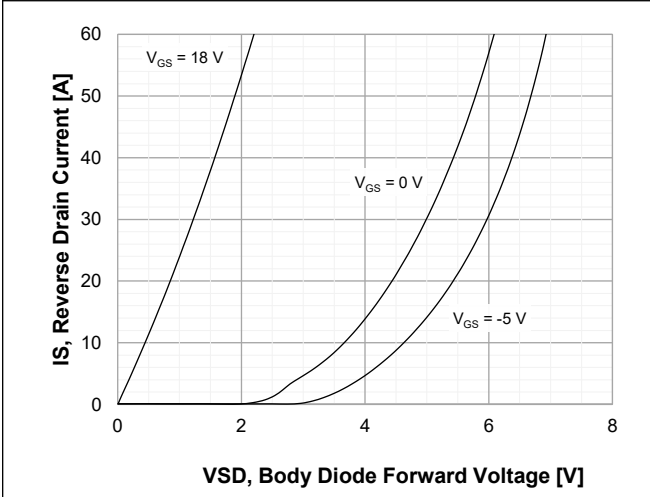


Figure 8. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 25^{\circ}\text{C}$

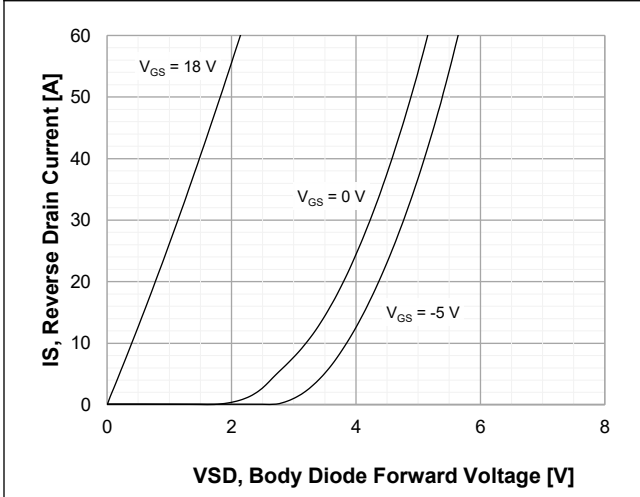


Figure 9. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 125^{\circ}\text{C}$

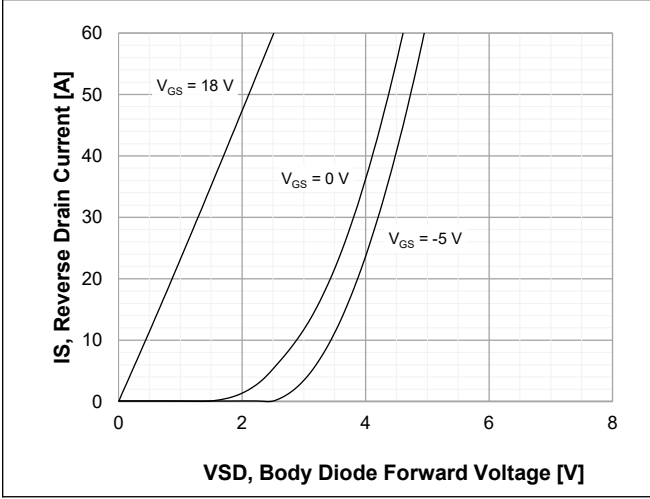


Figure 10. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 175^{\circ}\text{C}$

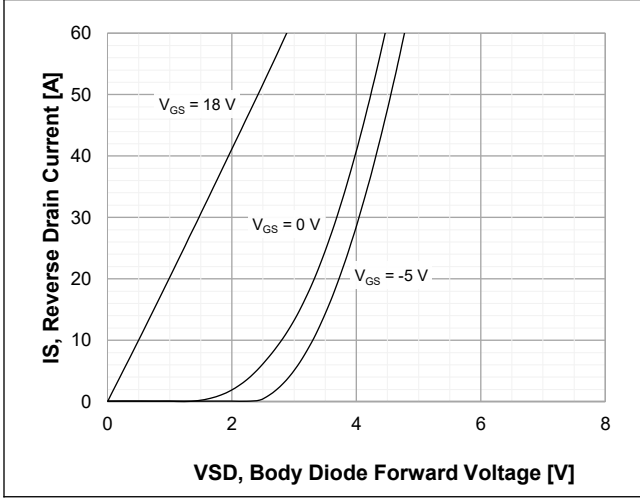


Figure 11. Threshold Voltage vs. Temperature

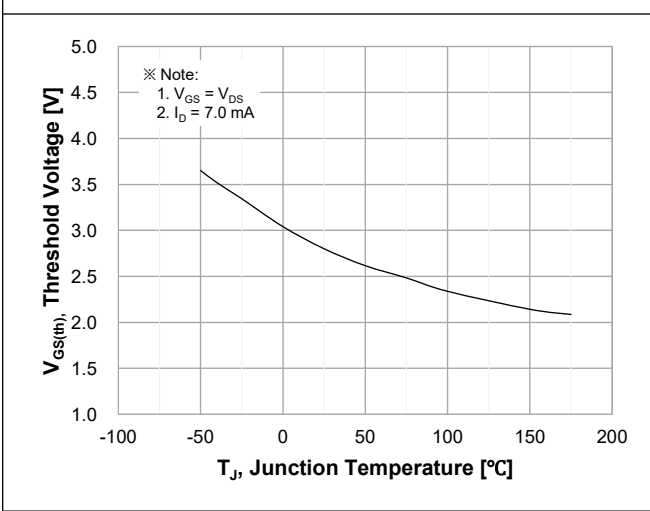
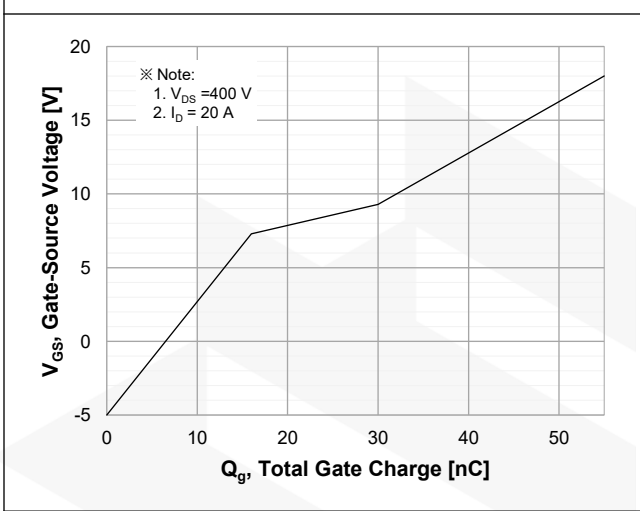


Figure 12. Gate Charge Characteristics



4. Typical Performance Characteristics

Figure 13. Stored Energy in Output Capacitance

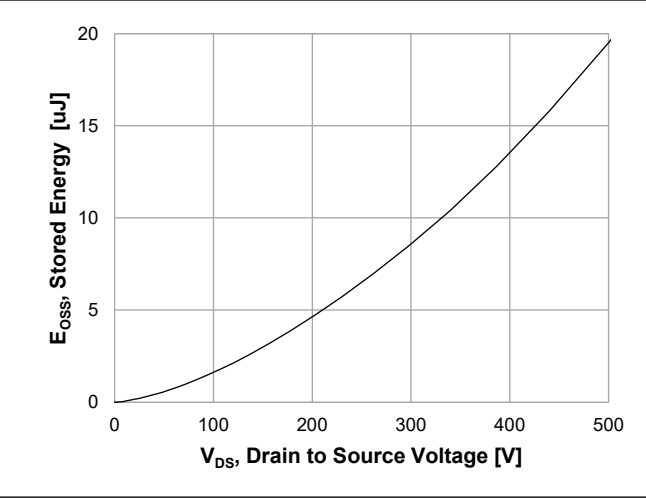


Figure 14. Capacitance Characteristics

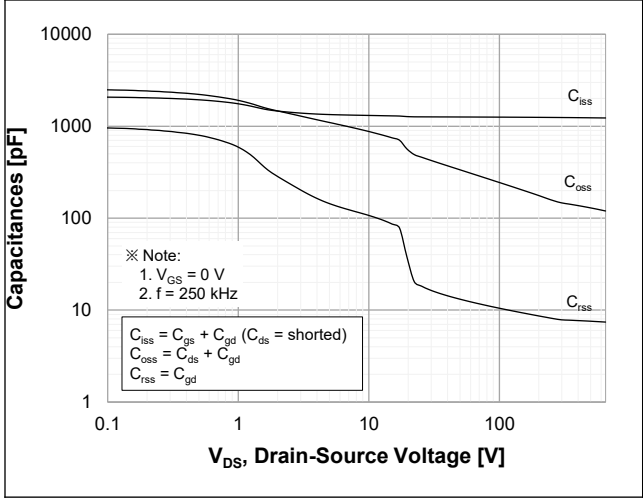


Figure 15. Continuous Drain Current Derating vs. Case Temperature

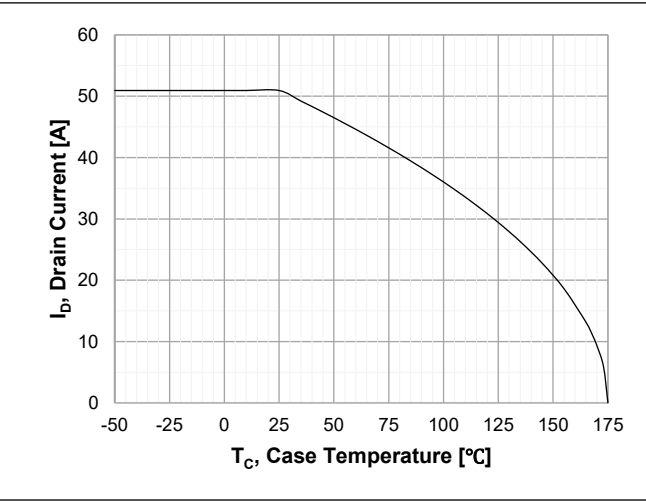


Figure 16. Maximum Power Dissipation Derating vs. Case Temperature

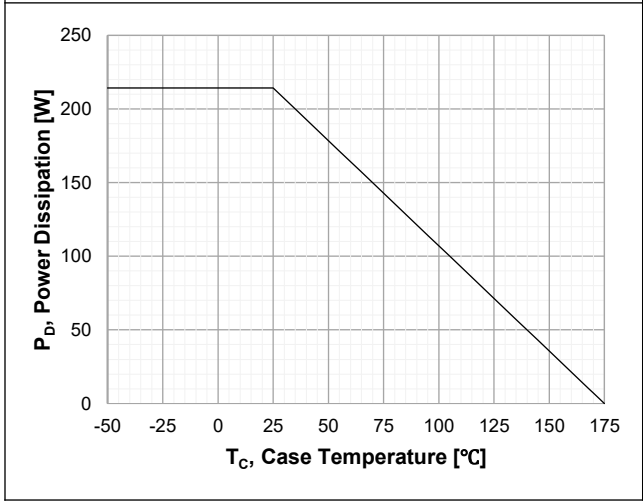


Figure 17. Typ. Switching Losses vs. Drain Current

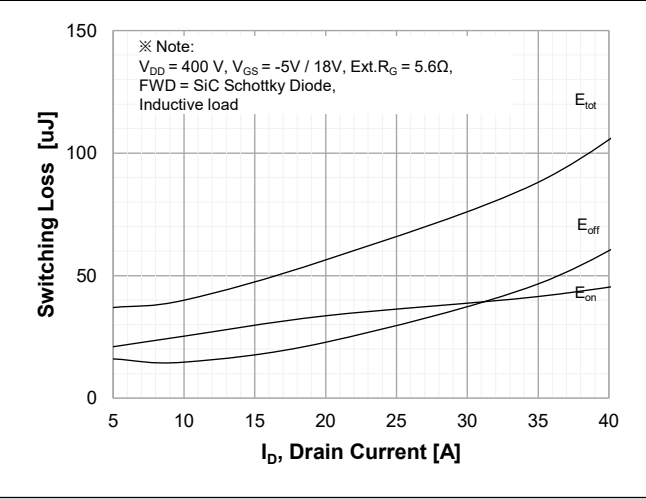
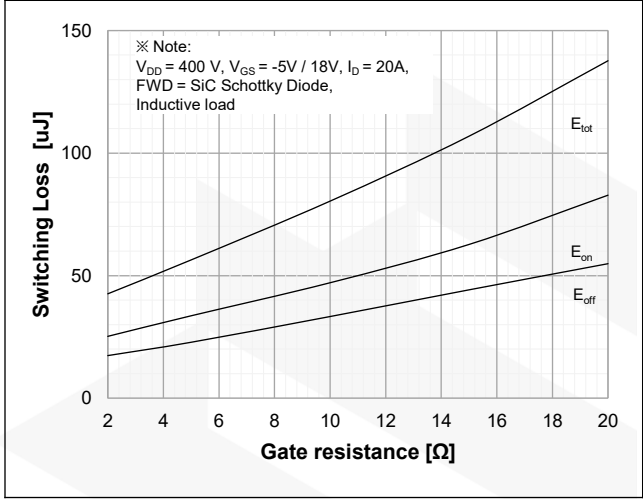


Figure 18. Typ. Switching Losses vs. Gate Resistance



4. Typical Performance Characteristics

Figure 19. Typ. Switching Losses vs. Drain Current

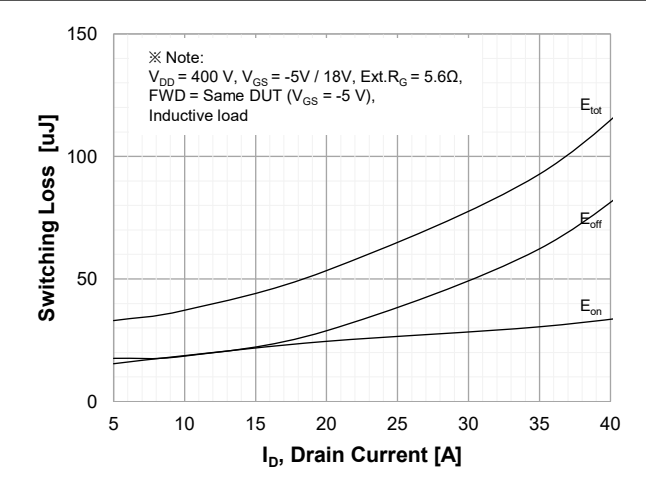


Figure 20. Typ. Switching Losses vs. Gate Resistance

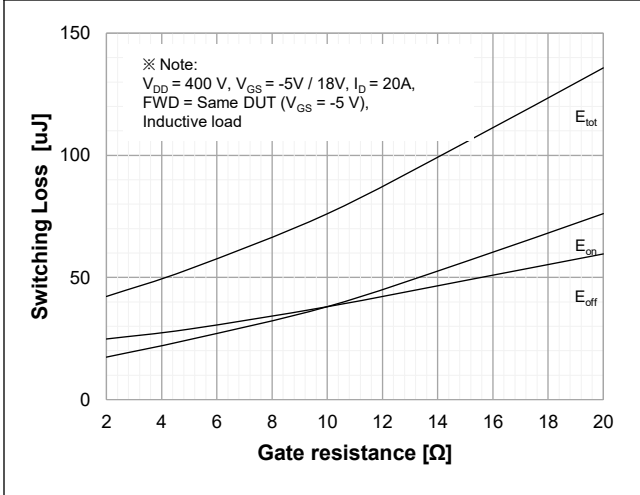


Figure 21. Maximum Safe Operating Area

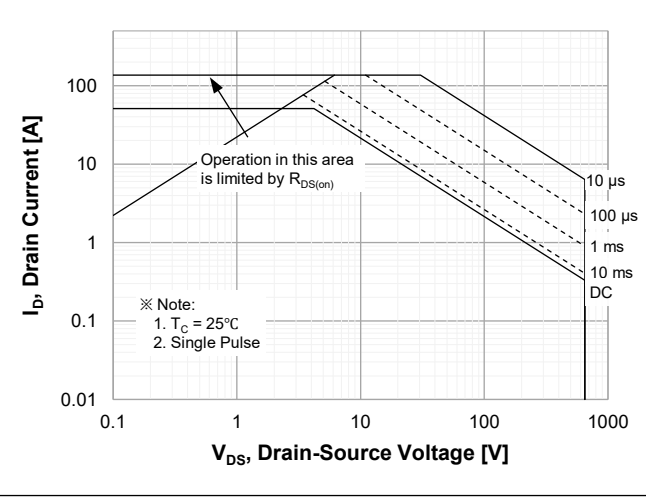
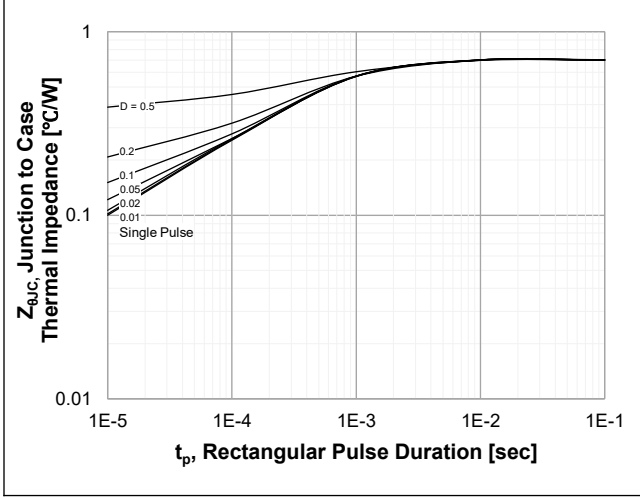


Figure 22. Transient Thermal Response Curve



5. Testing conditions

Figure 23. Inductive Load Switching Test Circuit and Waveforms

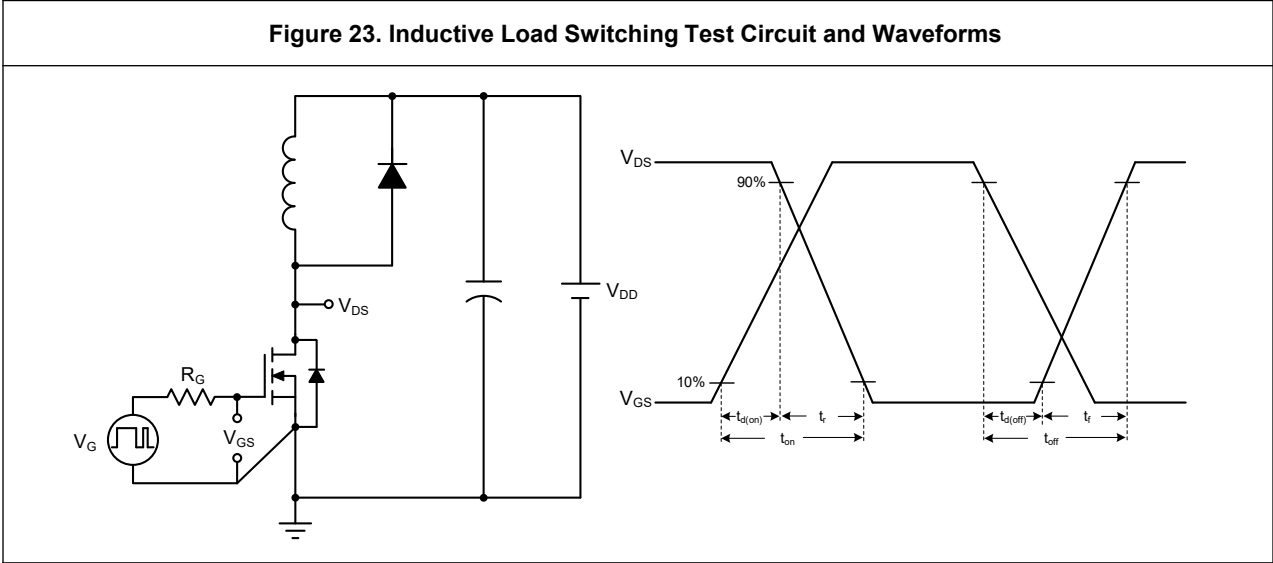
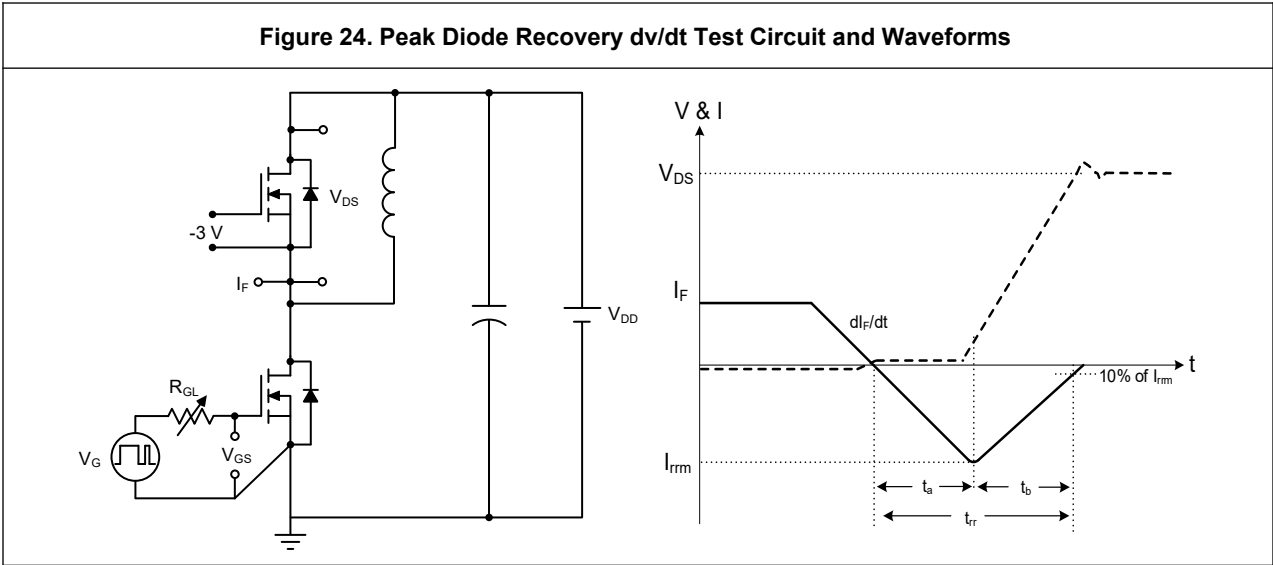
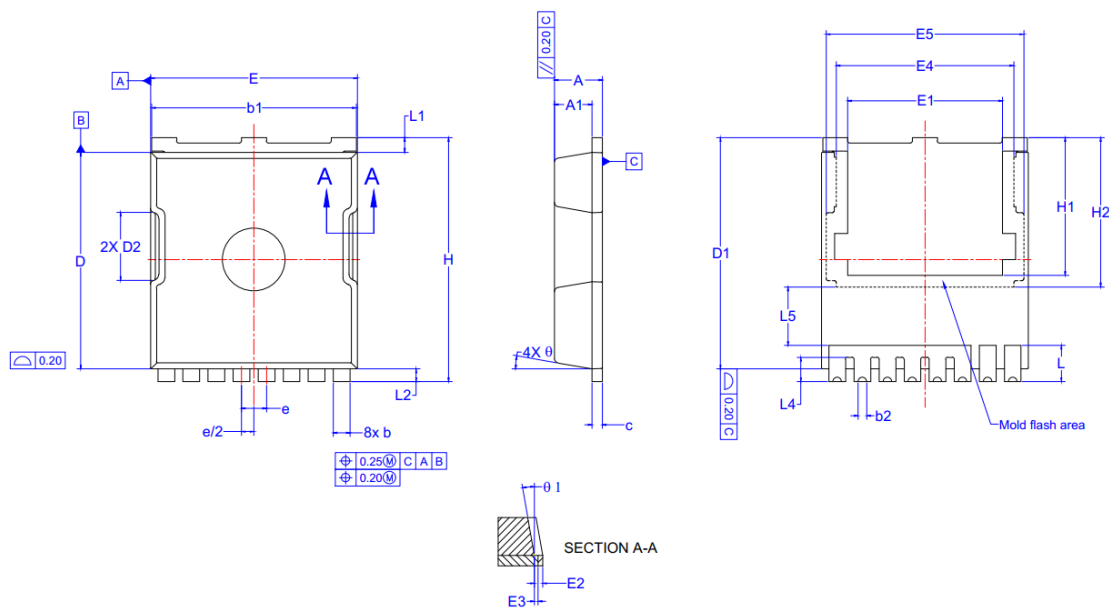


Figure 24. Peak Diode Recovery dv/dt Test Circuit and Waveforms



6. Package Outlines – TOLL

* Dimensions in millimeters



SYMBOL	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
A1	1.70	1.80	1.90
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.36	0.41	0.51
c	0.40	0.50	0.60
D	10.28	10.38	10.48
D1	10.98	11.08	11.18
D2	3.30		
E	9.80	9.90	10.00
E1	7.32	7.42	7.52
E2	0.30	0.40	0.50
E3	0.15	0.18	0.21
E4	8.50		
E5	9.46		
e	1.20 BASIC		
H	11.58	11.68	11.78
H1	6.55	6.65	6.75
H2	7.05	7.15	7.25
L	1.63	1.73	1.83
L1	0.60	0.70	0.80
L2	0.50	0.60	0.70
L4	1.00	1.15	1.30
L5	2.70	2.80	2.90
N	8		
θ	10° REF.		
θ1	10° REF.		

* Dimensions in millimeters