

Product Highlight

Features

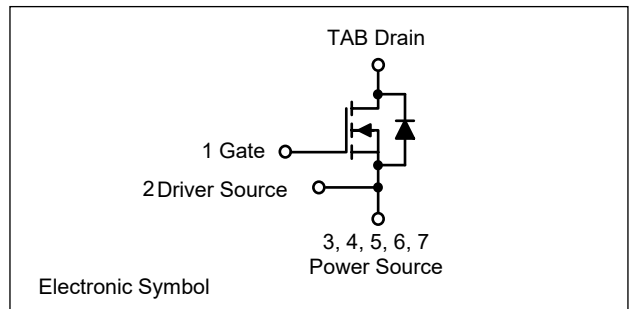
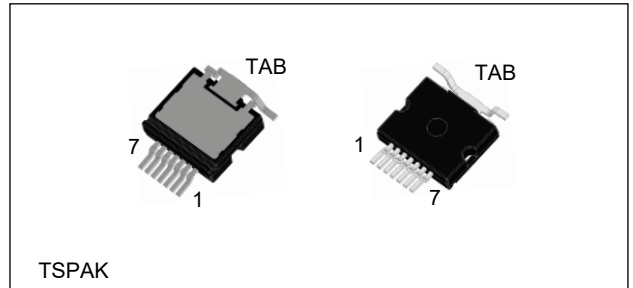
- High switching speed with a low gate charge
- Isolated substrate / High dielectric strength
- Isolation rating of 3.6kVrms
- 100% Avalanche Tested
- Pb-free, Halogen Free, and RoHS Compliant
- AEC Q101 Qualified

Benefits

- Top-side-cooling package
- Longer clearance / creepage distance
- Kelvin source connection
- Higher frequency applicability
- Easy heatsink assembly with thermal grease

Applications

- Automotive applications (OBC, e-Comp, DC/DC)
- Solar inverter
- EV charging station
- UPS, Industrial power supply



Key Parameters

$BV_{DSS, Tc=25^{\circ}C}$	$I_{D, Tc=25^{\circ}C}$	$R_{DS(on), typ}$	$Q_{g, typ}$
1200 V	57 A	40 mΩ	62 nC

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
AMSUN120R040T2RH	N120R040T2	TSPAK	Tape and Reel	600 units

Absolute Maximum Ratings ($T_C = 25^{\circ}C$ unless otherwise noted)

Symbol	Parameter		Value	Unit
V_{DSS}	Drain to Source Voltage		1200	V
V_{GS}	Gate to Source Voltage (DC)		-10 / +22	V
V_{GSop}	Recommended Operation Value		-5...-3 / +18	V
I_D	Drain Current	Continuous ($T_C = 25^{\circ}C$)	57*	A
		Continuous ($T_C = 100^{\circ}C$)	40*	
I_{DM}	Drain Current	Pulsed (Note1)	142*	A
P_D	Power Dissipation	($T_C = 25^{\circ}C$)	272	W
		Derate Above 25°C	1.9	W/°C
T_J	Operating Temperature Range		-55 to 175	°C

*Limited by maximum junction temperature.

Note 1. Repetitive rating: pulse-width limited by maximum junction temperature.

Note 2. LF discoloration and Picker Circle Printing allowed.

1. Package

Temperature Ratings

Symbol	Parameter	Value	Unit
T _{STG}	Storage Temperature Range	-55 to 175	°C
T _L	Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds	260	°C

Thermal Characteristics

Symbol	Parameter	Value	Unit
R _{θJC}	Thermal Resistance, Junction to Case, Max.	0.55	°C/W
R _{θJA}	Thermal Resistance, Junction to Ambient, Max.	40	

2. MOSFET

Electrical Characteristics (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0 V, I _D = 1 mA	1200			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 1200 V, V _{GS} = 0 V		1	100	μA
		V _{DS} = 1200 V, V _{GS} = 0 V, T _J = 175°C		10		
I _{GSS}	Gate-Source Leakage Current	V _{GS} = +22 V, V _{DS} = 0 V			+100	nA
		V _{GS} = -10 V, V _{DS} = 0 V			-100	
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 10 mA (tested after V _{GS} = 22 V, 1 ms pulse)	2.0	3.0	4.5	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 18 V, I _D = 28 A		40.0	54.0	mΩ
		V _{GS} = 18 V, I _D = 28 A, T _J = 175°C		64.0		
		V _{GS} = 15 V, I _D = 28 A		55.5		
g _{fs}	Transconductance	V _{DS} = 20 V, I _D = 28 A		16.9		S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 800 V, V _{GS} = 0 V, f = 250 kHz		1668		pF
C _{oss}	Output Capacitance			105		
C _{rss}	Reverse Capacitance			4		
E _{oss}	Stored Energy in Output Capacitance	V _{DS} = 0 V to 800 V, V _{GS} = 0 V		42		μJ
C _{o(er)}	Energy Related Output Capacitance			132		pF
C _{o(tr)}	Time Related Output Capacitance			201		
Q _{g(tot)}	Total Gate Charge	V _{DS} = 800 V, I _D = 28 A, V _{GS} = -3 V / 18 V, Inductive load		62		nC
Q _{gs}	Gate to Source Charge			20		
Q _{gd}	Gate to Drain “Miller” Charge			14		
R _G	Internal Gate Resistance	f = 1 MHz, V _{AC} = 30 mV		3.0		Ω
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} = 800 V, I _D = 28 A, V _{GS} = -3 V / 18 V, R _G = 6.8 Ω, Inductive load		19		ns
t _r	Turn-On Rise Time			15		
t _{d(off)}	Turn-Off Delay Time			35		
t _f	Turn-Off Fall Time			8		
E _{on}	Turn-on Switching Energy			158		μJ
E _{off}	Turn-off Switching Energy			100		
E _{tot}	Total Switching Energy			258		

3. Body Diode

Electrical Characteristics (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Source-Drain Diode Characteristics						
I _S	Continuous Diode Forward Current	V _{GS} = -3 V			57*	A
I _{SM}	Pulsed Diode Forward Current	V _{GS} = -3 V (Note1)			142*	
V _{SD}	Diode Forward Voltage	V _{GS} = -3 V, I _{SD} = 28 A		4.3		V
t _{rr}	Reverse Recovery Time	V _{DD} = 800 V, I _{SD} = 28 A, dI _F /dt = 3000 A/μs		15		ns
Q _{rr}	Reverse Recovery Charge			219		nC
I _{rrm}	Peak Reverse Recovery Current			24		A

*Limited by maximum junction temperature.

4. Typical Performance Characteristics

Figure 1. On-Region Characteristics $T_J = -40^{\circ}\text{C}$

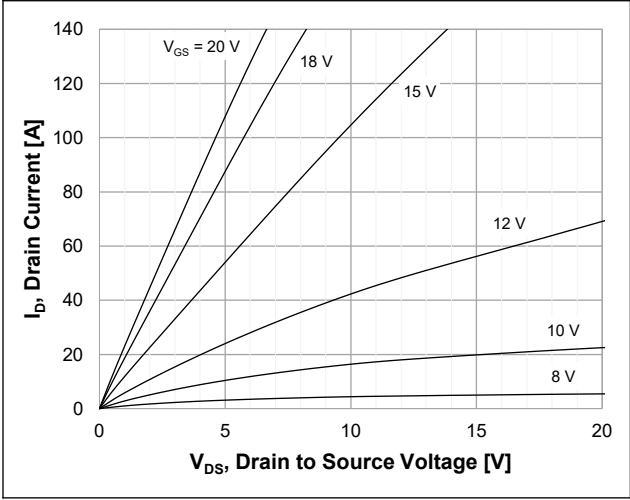


Figure 2. On-Region Characteristics $T_J = 25^{\circ}\text{C}$

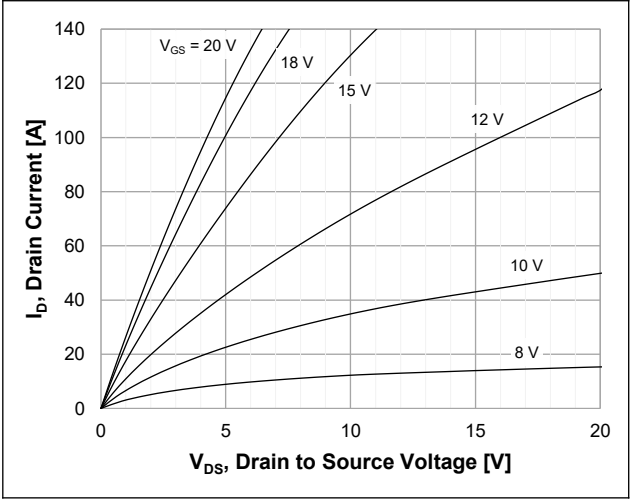


Figure 3. On-Region Characteristics $T_J = 175^{\circ}\text{C}$

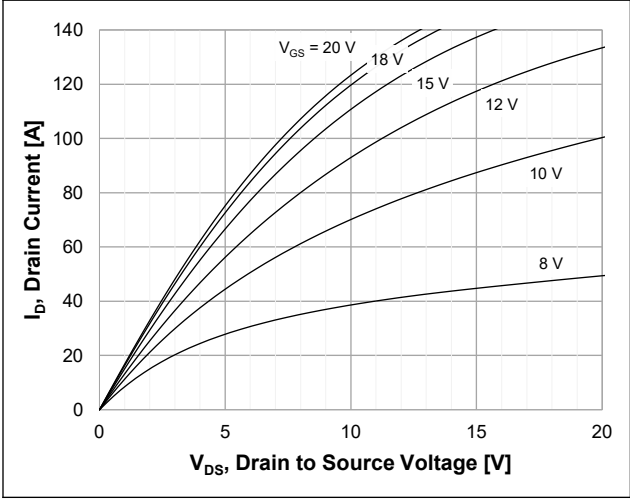


Figure 4. Normalized On-Resistance Characteristics vs. Temperature

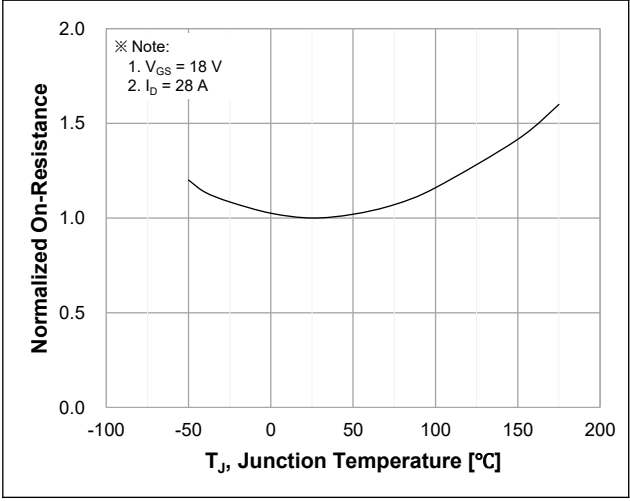


Figure 5. Transfer Characteristics

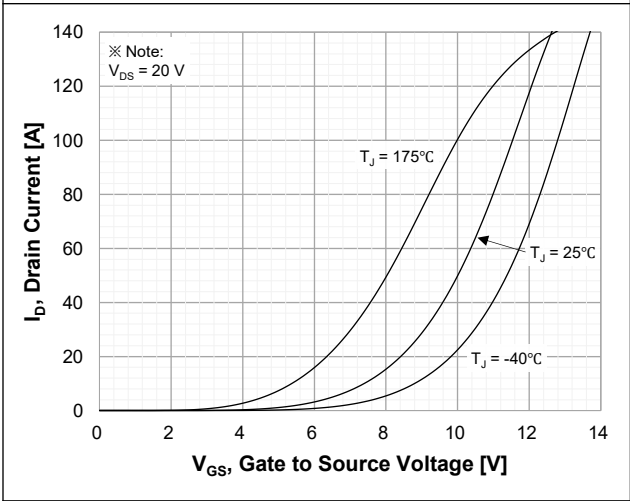
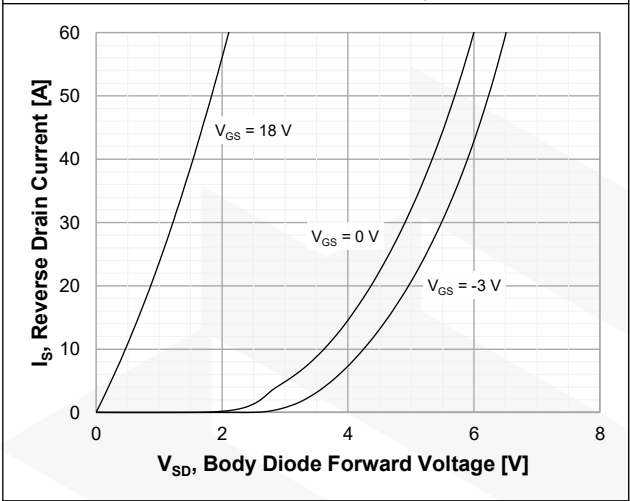


Figure 6. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = -40^{\circ}\text{C}$



4. Typical Performance Characteristics

Figure 7. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 25^{\circ}\text{C}$

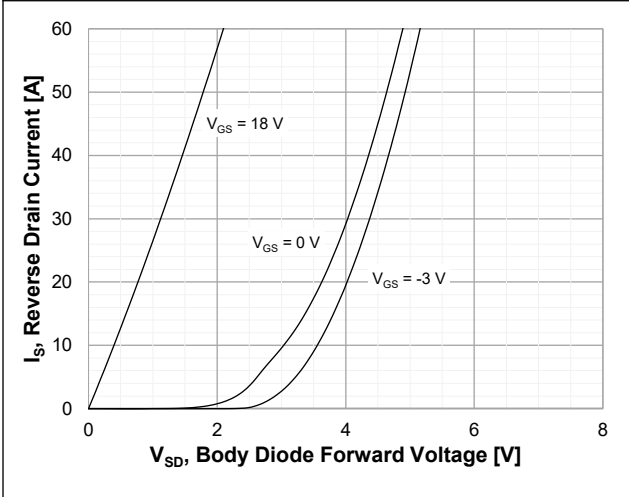


Figure 8. Diode Forward Voltage Characteristics vs. Source-Drain Current $T_J = 175^{\circ}\text{C}$

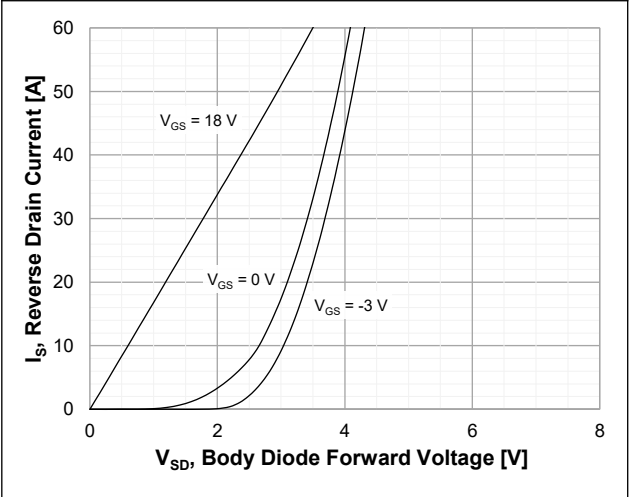


Figure 9. Threshold Voltage vs. Temperature

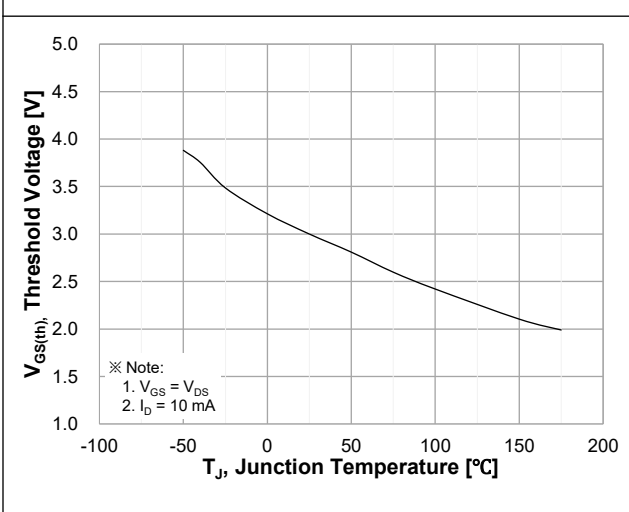


Figure 10. Gate Charge Characteristics

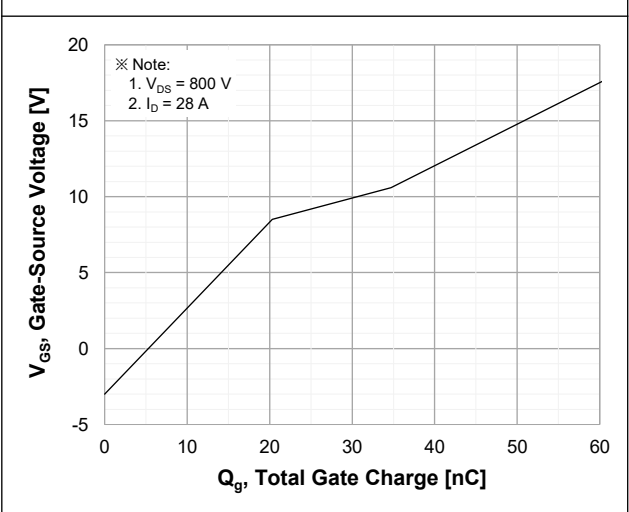


Figure 11. Stored Energy in Output Capacitance

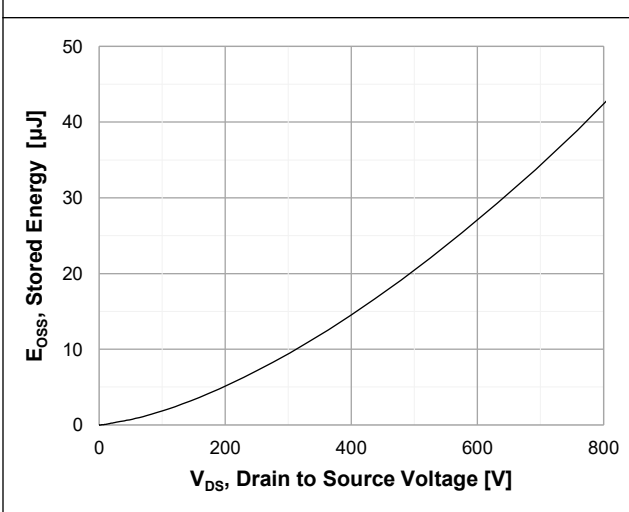
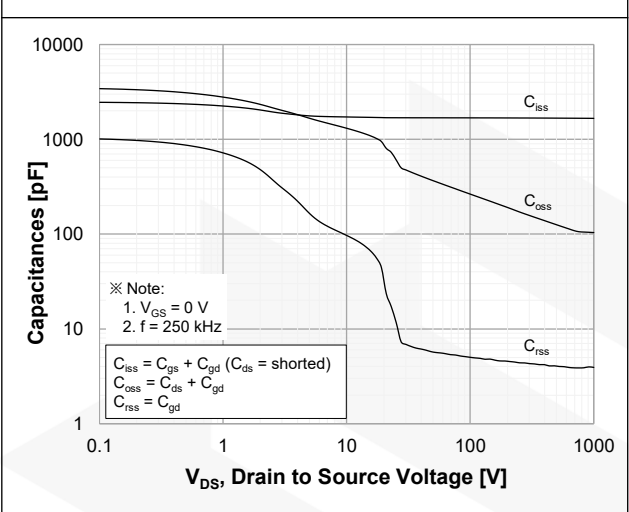
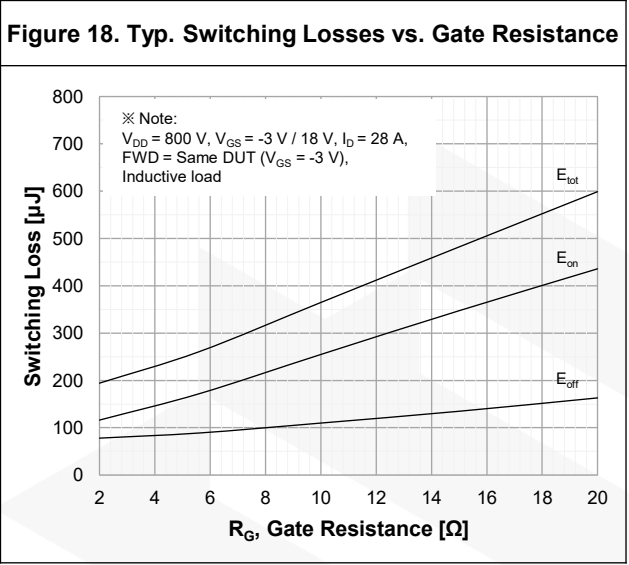
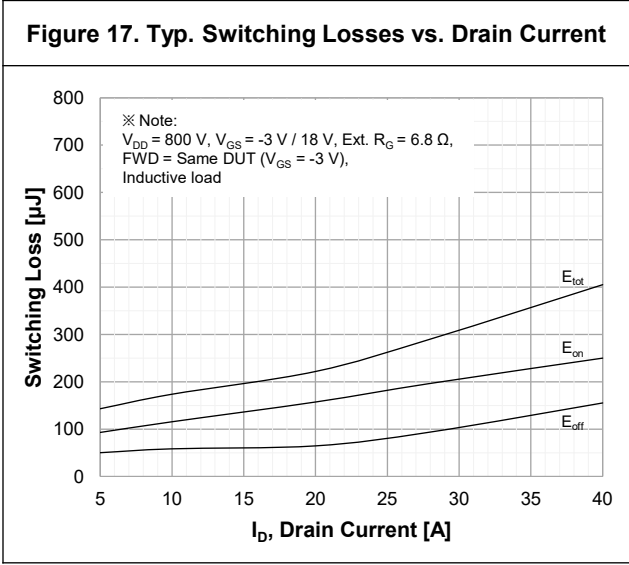
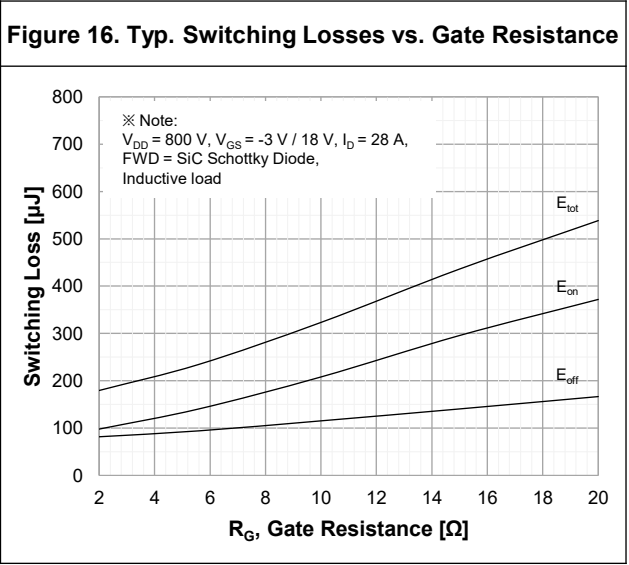
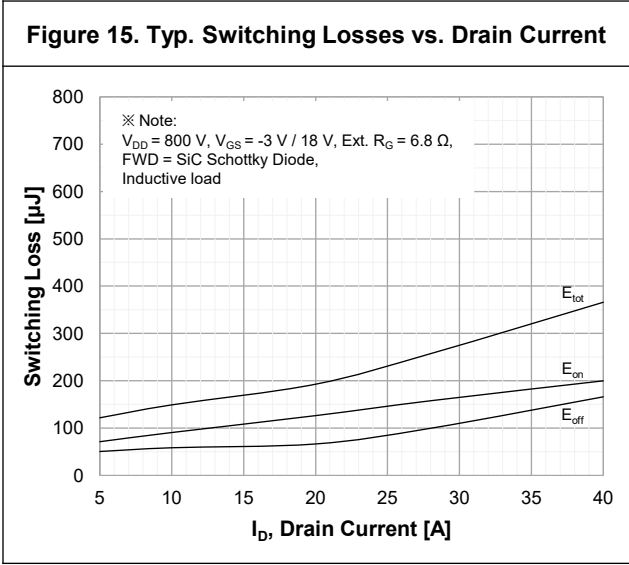
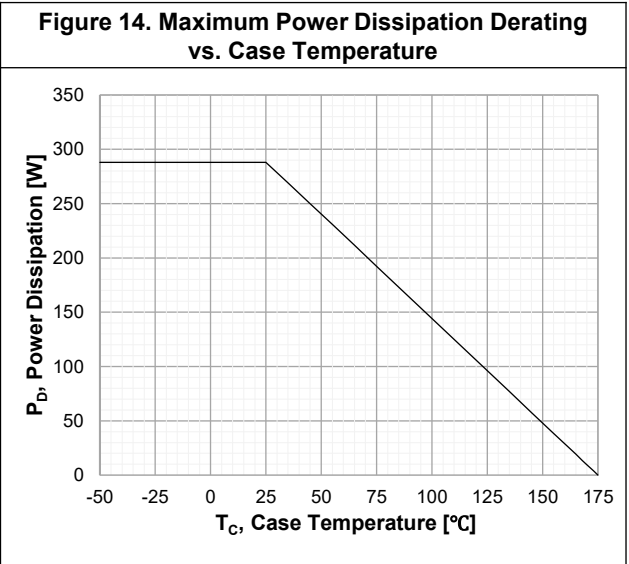
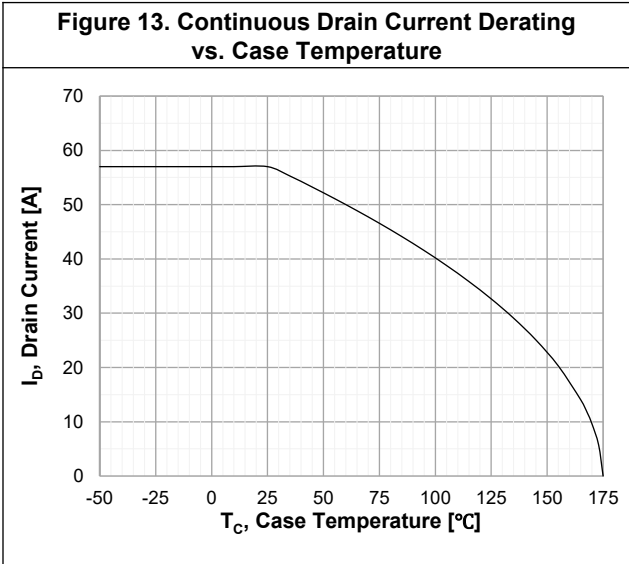


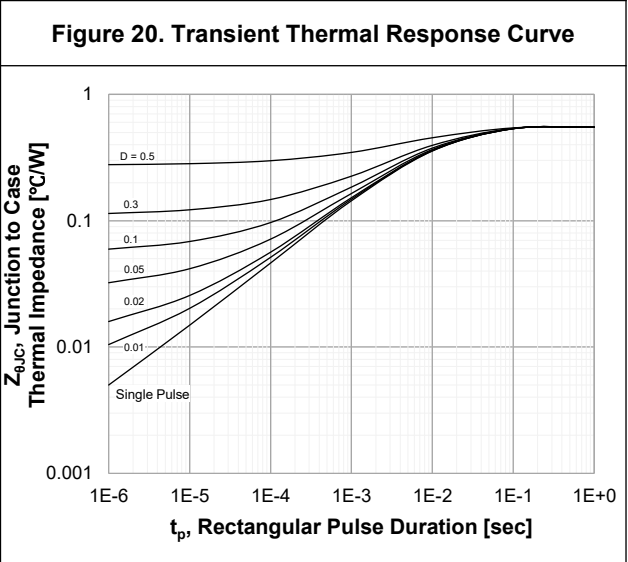
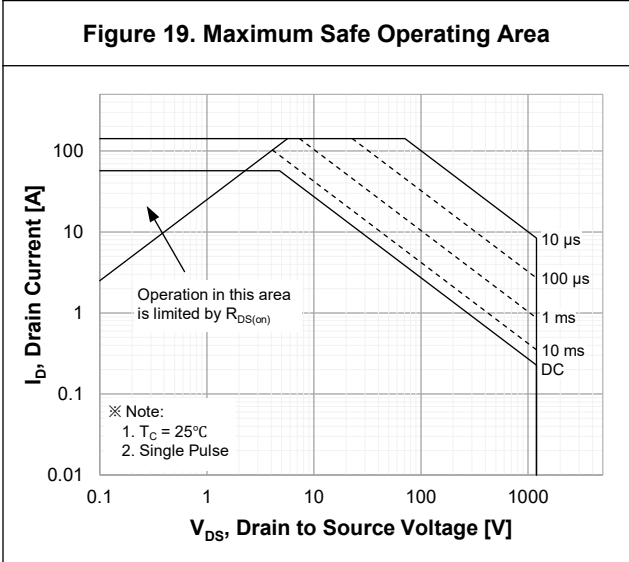
Figure 12. Capacitance Characteristics



4. Typical Performance Characteristics



4. Typical Performance Characteristics



5. Testing conditions

Figure 21. Inductive Load Switching Test Circuit and Waveforms

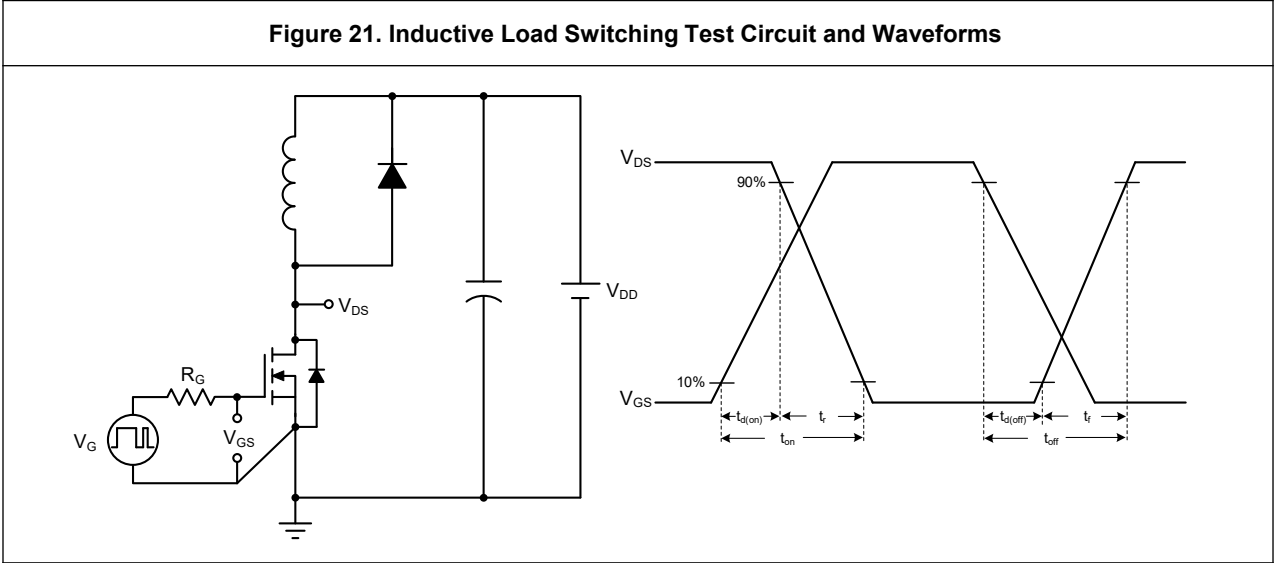
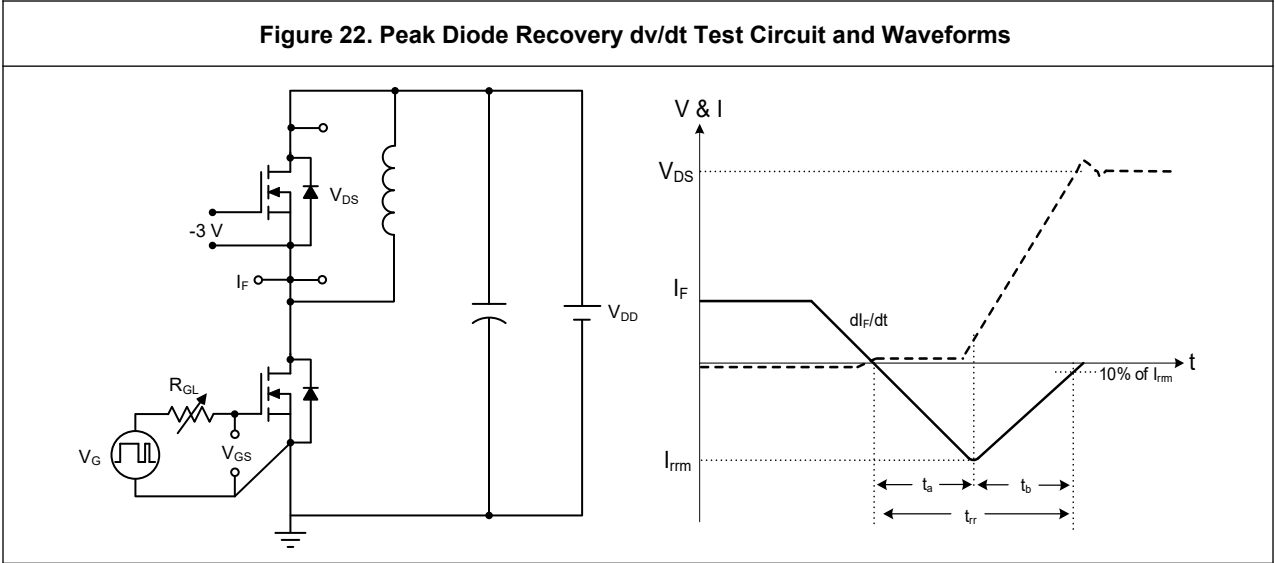
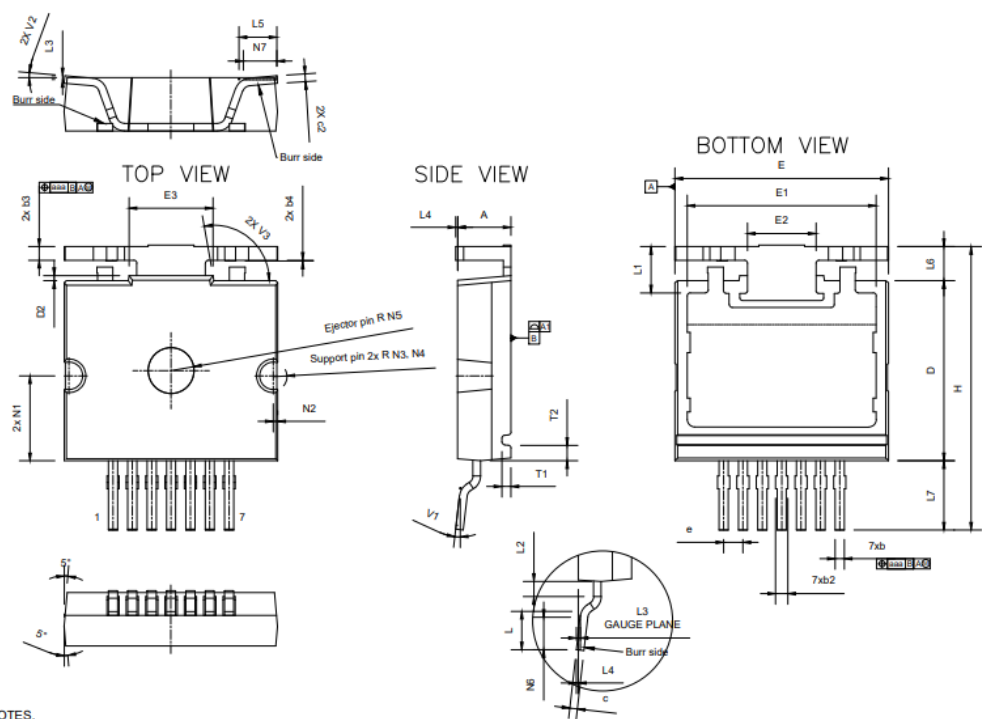


Figure 22. Peak Diode Recovery dv/dt Test Circuit and Waveforms



6. Package Outlines – TSPAK

* Dimensions in millimeters



NOTES.
1. DIMENSION AND TOLERANCE CONFORM TO ASME Y14.5M
2. CONTROLLING DIMENSIONS : MILLIMETER. CONVERTED INCH.
DIMENSION ARE NOT NECESSARILY EXACT.
3. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE
TERMINALS.
4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
PROTRUSIONS, OR GATE BURRS.

SYMBOL	Common		
	DIMENSIONS MILLIMETER		
	MIN.	NOM.	MAX.
A	3.40	3.50	3.60
A1	0.05		
b	0.50	0.60	0.70
b2	0.70	0.80	0.90
b3	0.80	0.90	0.98
b4	Ref only. 0.05mm		
c	0.40	0.50	0.60
c2	0.40	0.50	0.60
D	11.70	11.80	11.90
D1	7.78	7.88	7.98
D2	0.24	0.34	0.44
E	13.90	14.00	14.10
E1	12.30	12.40	12.50
E2	4.45	4.50	4.55
E3	5.45	5.50	5.55
e	1.27		
H	18.00	18.58	19.00
L	2.42	2.52	2.62
L1	3.04		
L2	0.90	1.00	1.10
L3	0.26		
L4	0.075	0.125	0.175
L5	2.38	2.48	2.58
L6	2.14	2.24	2.34
L7	4.44	4.54	4.64
N1	5.46	5.56	5.66
N2	0.25	0.30	0.35
N3	0.80	0.90	1.00
N4	1.00	1.10	1.20
N5	1.40	1.50	1.60
N6	2.185	2.285	2.385
N7	2.10	2.20	2.30
T1	0.60		
T2	1.00		
T3	0.60	0.70	0.80
V1	0°	5°	8°
V2	0°	6°	8°
V3	100°		
aaa	0.1		

7. Footprint Guide – TSPAK

* Dimensions in millimeters

