

General Description

This N-channel MOSFET is produced using advanced Magnachip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

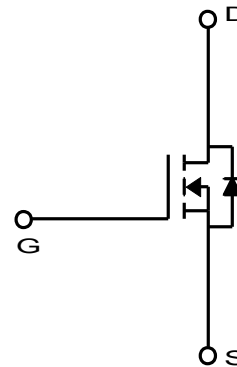
This device is suitable for SMPS, high speed switching and automotive or industrial purpose applications.

Features

- $V_{DS} = 250V$
- $I_D = 10.2A$
- $R_{DS(ON)} \leq 0.28\Omega @ V_{GS} = 10V$
- AEC-Q101 qualified
- Pb-free plating, halogen-free

Applications

- Power supply
- Motor control
- Car inverter
- High current, high speed switching



Absolute maximum ratings ($T_c = 25^\circ C$ unless otherwise specified)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	250	V
Gate-source voltage		V_{GSS}	± 30	V
Continuous drain current	$T_c = 25^\circ C$	I_D	10.2	A
	$T_c = 100^\circ C$		7.2	A
Pulsed drain current ⁽¹⁾		I_{DM}	40.8	A
Power dissipation	$T_c = 25^\circ C$	P_D	144	W
	Derate above $25^\circ C$		0.96	W/ $^\circ C$
Peak diode recovery dv/dt ⁽²⁾		dv/dt	4.5	V/ns
Repetitive avalanche energy ⁽¹⁾		E_{AR}	6.94	mJ
Avalanche current ⁽¹⁾		I_{AR}	10.2	A
Single pulse avalanche energy ⁽⁴⁾		E_{AS}	550	mJ
Junction and storage temperature range		T_j, T_{stg}	-55~175	$^\circ C$

Thermal characteristics

Characteristics	Symbol	Rating	Unit
Thermal resistance, junction-to-ambient ⁽¹⁾	$R_{\theta JA}$	41.9	$^\circ C/W$
Thermal resistance, junction-to-case ⁽¹⁾	$R_{\theta JC}$	1.04	

Ordering information

Part Name	Marking	Temp. Range	Package	Packing	RoHS Status
AMDD14N25CRH	14N25C	-55~175°C	TO-252(DPAK)	Reel and tape	Compliant

Electrical characteristics ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static characteristics						
Drain-source breakdown voltage	BV_{DSS}	$I_D = 250\mu\text{A}$, $V_{GS} = 0\text{V}$	250	-	-	V
Gate threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250\mu\text{A}$	2.0	-	4.0	
Drain cut-off current	I_{DSS}	$V_{DS} = 250\text{V}$, $V_{GS} = 0\text{V}$	-	-	1	μA
Gate leakage current	I_{GSS}	$V_{GS} = \pm 30\text{V}$, $V_{DS} = 0\text{V}$	-	-	100	nA
Drain-source on-resistance	$R_{DS(ON)}$	$V_{GS} = 10\text{V}$, $I_D = 5.1\text{A}$	-	0.22	0.28	Ω
Forward transconductance	g_{fs}	$V_{DS} = 30\text{V}$, $I_D = 5.1\text{A}$	-	8.5	-	S
Dynamic characteristics						
Total gate charge	Q_g	$V_{DS} = 200\text{V}$, $I_D = 14.0\text{A}$, $V_{GS} = 10\text{V}$	-	20.0	-	nC
Gate-source charge	Q_{gs}		-	4.5	-	
Gate-drain charge	Q_{gd}		-	8.9	-	
Input capacitance	C_{iss}	$V_{DS} = 25\text{V}$, $V_{GS} = 0\text{V}$, $f = 1.0\text{MHz}$	-	741	-	pF
Reverse transfer capacitance	C_{rss}		-	15	-	
Output capacitance	C_{oss}		-	142	-	
Turn-on delay time	$t_{d(on)}$	$V_{GS} = 10\text{V}$, $V_{DS} = 125\text{V}$, $I_D = 14.0\text{A}$, $R_G = 25\Omega^{(3)}$	-	13	-	ns
Rise time	t_r		-	42	-	
Turn-off delay time	$t_{d(off)}$		-	44	-	
Fall time	t_f		-	28	-	
Drain-source body diode characteristics						
Maximum continuous drain to source diode forward current	I_S		-	-	10.2	A
Source-drain diode forward voltage	V_{SD}	$I_S = 10.2\text{A}$, $V_{GS} = 0\text{V}$	-	-	1.4	V
Body diode reverse recovery time	t_{rr}	$I_F = 14.0\text{A}$, $di/dt = 100\text{A}/\mu\text{s}^{(3)}$	-	174	-	ns
Body diode reverse recovery charge	Q_{rr}		-	1.0	-	μC

Note :

- Pulse width is based on $R_{\theta JC}$ & $R_{\theta JA}$ and the maximum allowed junction temperature of 175°C
- Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$, pulse width limited by junction temperature $T_{j, \max} = 175^\circ\text{C}$
- $I_{SD} \leq 10.2\text{A}$, $di/dt \leq 300\text{A}/\mu\text{s}$, $V_{DD} \leq BV_{DSS}$, $R_G = 25\Omega$, Starting $T_j = 25^\circ\text{C}$
- $L = 8.5\text{mH}$, $I_{AS} = 10.2\text{A}$, $V_{DD} = 50\text{V}$, $R_G = 26\Omega$, Starting $T_j = 25^\circ\text{C}$

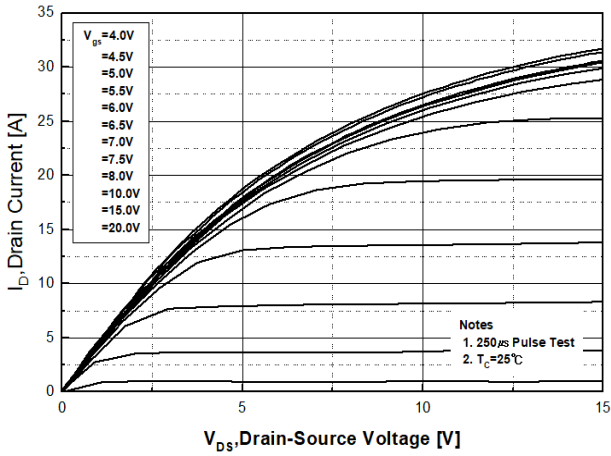


Fig.1 On-region Characteristics

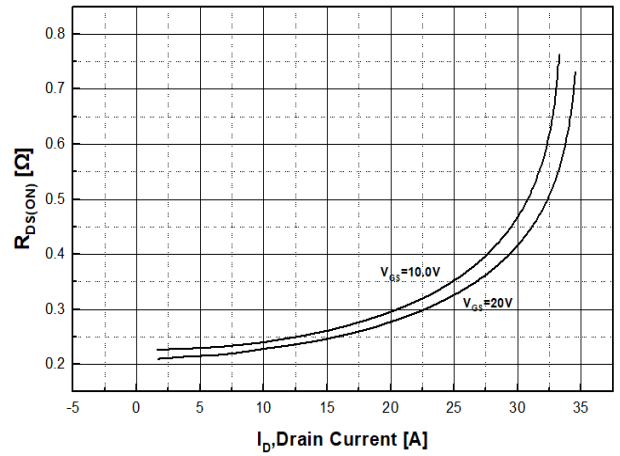


Fig.2 On-resistance variation with drain current and gate Voltage

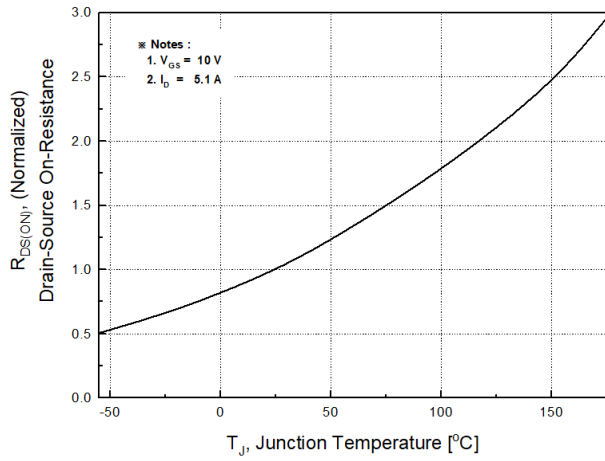


Fig.3 On-resistance vs. temperature

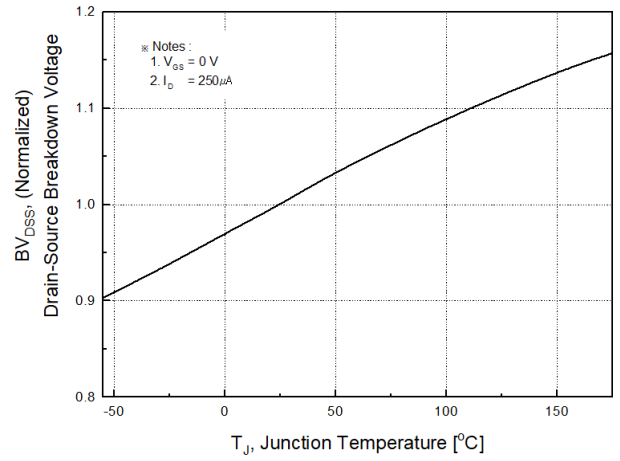


Fig.4 Breakdown voltage vs. temperature

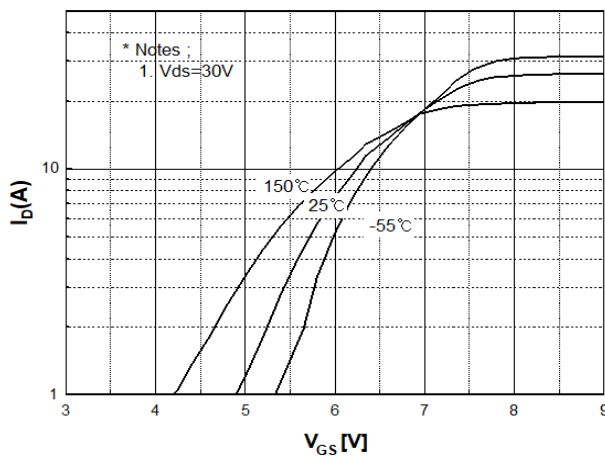


Fig.5 Transfer characteristics

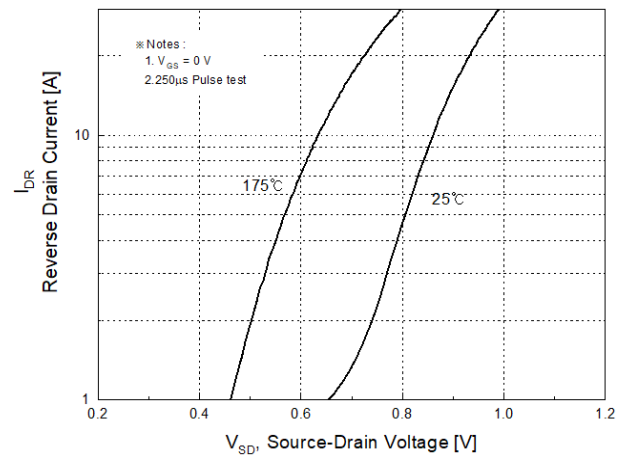


Fig.6 Body diode forward voltage variation with source current and temperature

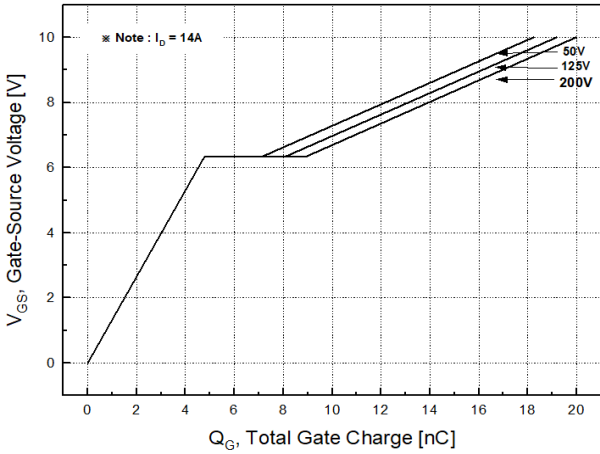


Fig.7 Gate charge characteristics

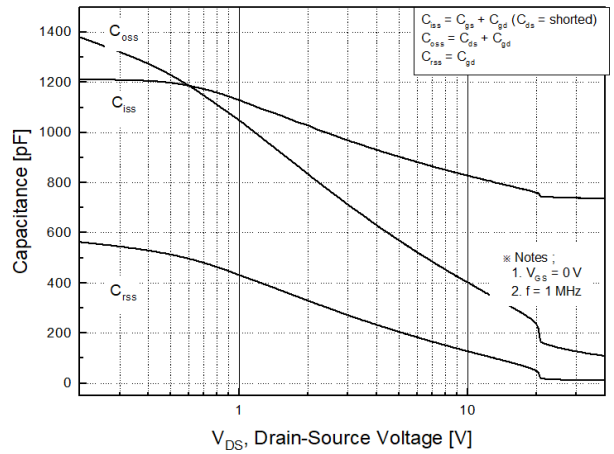


Fig.8 Capacitance characteristics

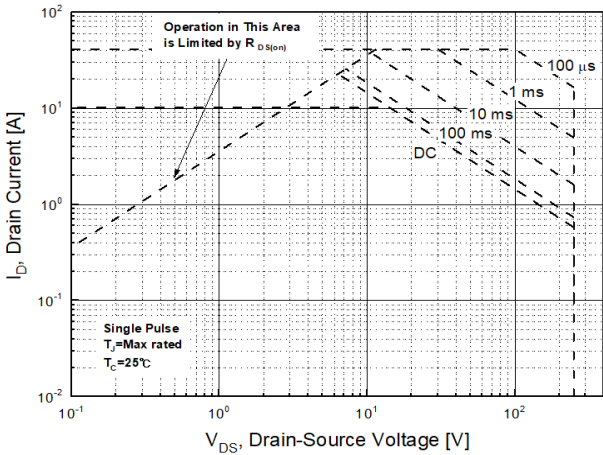


Fig.9 Maximum safe operating area

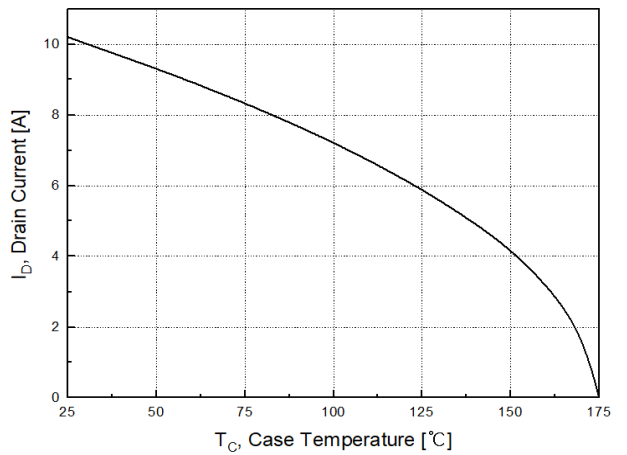


Fig.10 Maximum drain current vs. case temperature

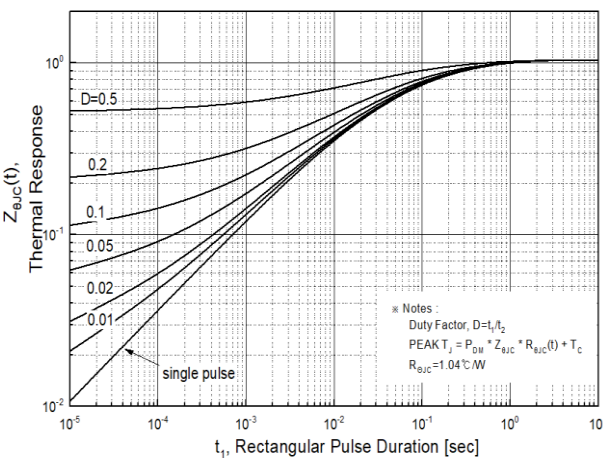


Fig.11 Transient thermal response curve

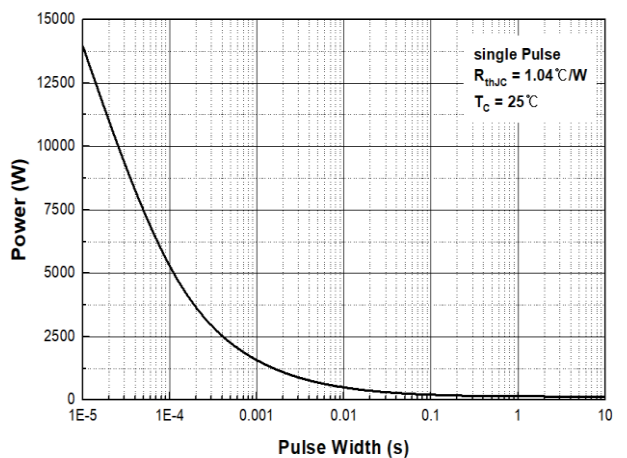
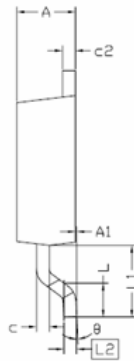
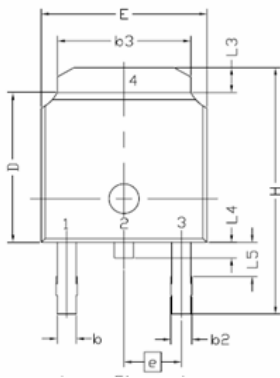


Fig.12 Single pulse maximum power dissipation

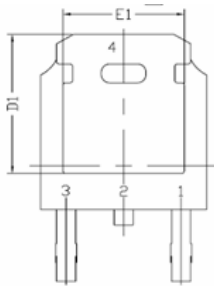
Physical dimension

TO-252

Dimensions are in millimeters, unless otherwise specified




Symbol	Min.	Nom.	Max.
E	6.35	-	6.73
L	1.40	1.52	1.78
L1	2.74 REF		
L2	0.508 BCS		
L3	0.89	-	1.27
L4	-	-	1.02
L5	1.14	-	1.52
D	5.97	6.10	6.22
H	9.40	-	10.41
b	0.64	-	0.89
b2	0.76	-	1.14
b3	4.95	-	5.46
e	2.286 BSC		
A	2.18	-	2.39
A1	-	-	0.13
c	0.46	-	0.61
c2	0.46	-	0.89
D1	5.21	-	-
E1	4.32	-	-
θ	0.00	-	10.00



DISCLAIMER:

The Products are not designed for use in hostile environments, including, without limitation, aircraft, nuclear power generation, medical appliances, and devices or systems in which malfunction of any Product can reasonably be expected to result in a personal injury. Seller's customers using or selling Seller's products for use in such applications do so at their own risk and agree to fully defend and indemnify Seller.

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