MDS3603

Single P-Channel Trench MOSFET, -30V, -12A, 10.1mΩ

General Description

The MDS3603 uses advanced Magnachip's MOSFET Technology to provide low on-state resistance.

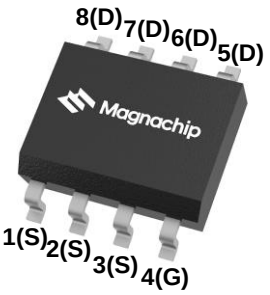
This device is suited for Power Management and load switching applications common in Notebook Computers and Portable Battery Packs.

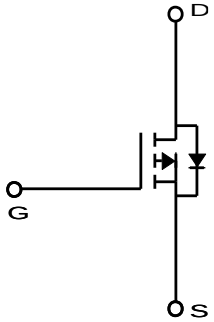
Features

- V_{DS} = -30V
- I_D = -12A @V_{GS} = -10V
- $R_{DS(ON)}$
  < 8.5mΩ @V_{GS} = -20V
  < 10.1mΩ @V_{GS} = -10V
  < 14.5mΩ @V_{GS} = -5V

Applications

- Load Switch
- General purpose applications
- Smart Module for Note PC Battery





Absolute Maximum Ratings (T_a =25°C unless otherwise noted)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	±25	V
Continuous Drain Current	(Note 1)	I_D	-12	A
Pulsed Drain Current		I_{DM}	-48	A
Power Dissipation		P_D	2.5	W
Single Pulse Avalanche Energy	(Note 2)	E_{AS}	112.5	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics		Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient	(Note 1)	$R_{\theta JA}$	50	°C/W
Thermal Resistance, Junction-to-Case		$R_{\theta JC}$	22	

Ordering Information

Part Number	Temp. Range	Package	Packing	Quantity	RoHS Status
MDS3603URH	-55~150°C	SOIC-8	Tape & Reel	3000 units	Halogen Free

Electrical Characteristics (T_a = 25°C unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	I_D = -250μA, V_{GS} = 0V	-30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250μA	-1.0	-1.8	-3.0	
Drain Cut-Off Current	I_{DSS}	V_{DS} = -30V, V_{GS} = 0V	-		-1	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±25V, V_{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	$R_{DS(ON)}$	V_{GS} = -20V, I_D = -12A	-	7.3	8.5	mΩ
		V_{GS} = -10V, I_D = -12A	-	8.3	10.1	
		V_{GS} = -5V, I_D = -10A		11.5	14.5	
Forward Transconductance	g_{FS}	V_{DS} = -5V, I_D = -10A		28	-	S
Dynamic Characteristics						
Total Gate Charge	Q_g	V_{DS} = -15V, I_D = -12A V_{GS} = -10V	-	38.4	-	nC
Gate-Source Charge	Q_{gs}		-	5.9	-	
Gate-Drain Charge	Q_{gd}		-	8.2	-	
Input Capacitance	C_{iss}	V_{DS} = -15V, V_{GS} = 0V, f = 1.0MHz	-	1788	-	pF
Reverse Transfer Capacitance	C_{rss}		-	268	-	
Output Capacitance	C_{oss}		-	445	-	
Turn-On Delay Time	$t_{d(on)}$	V_{GS} = -10V ,V_{DS} = -15V, R_L = 1.25Ω, R_{GEN} = 3Ω	-	15.3	-	ns
Turn-On Rise Time	t_r		-	13.0	-	
Turn-Off Delay Time	$t_{d(off)}$		-	61.6	-	
Turn-Off Fall Time	t_f		-	53.2	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	I_S = -1A, V_{GS} = 0V	-	-0.72	-1.0	V
Body Diode Reverse Recovery Time	t_{rr}	I_F = -12A, di/dt = 100A/μs	-	42.3		ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	40.7	-	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7)
2. Starting T_J=25°C, L=1mH, I_{AS}= -15A V_{DD}=-20V, V_{GS}=-10V.

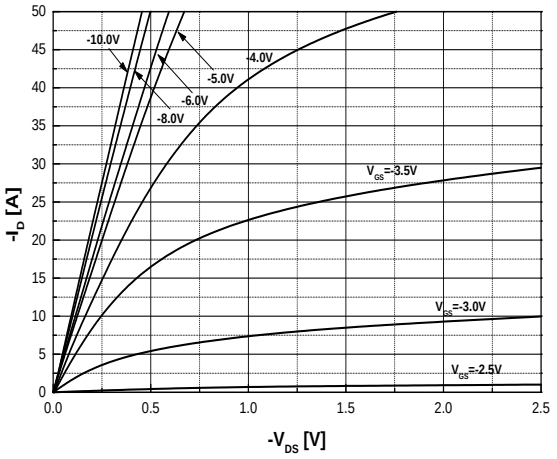


Fig.1 On-Region Characteristics

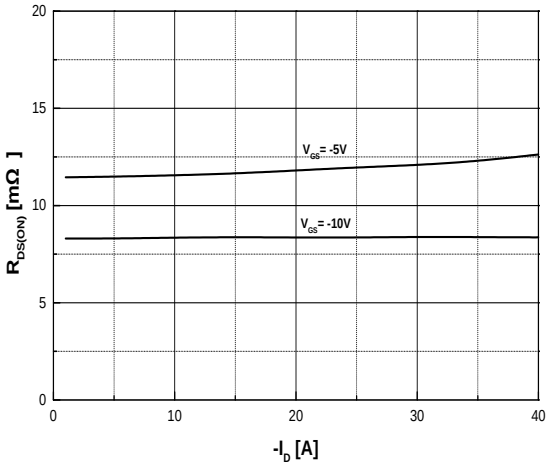


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

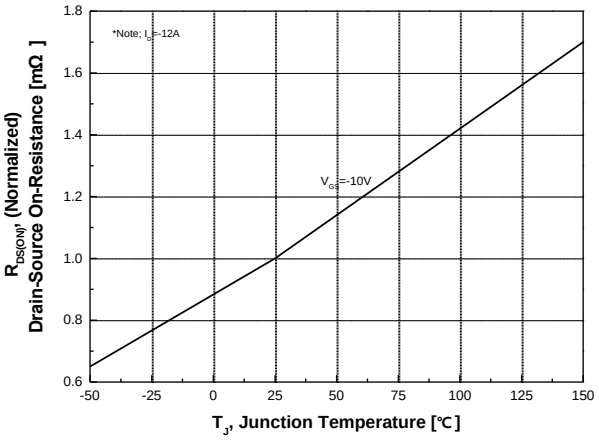


Fig.3 On-Resistance Variation with Temperature

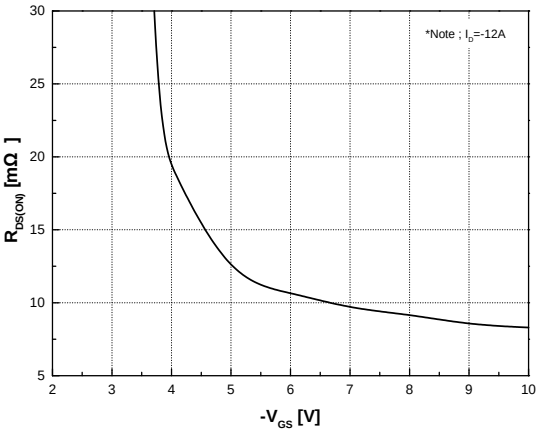


Fig.4 On-Resistance Variation with Gate to Source Voltage

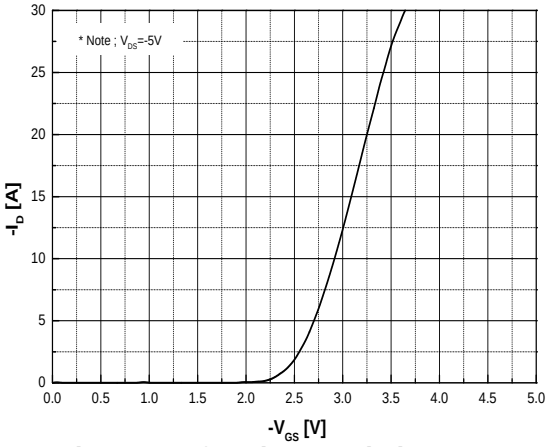


Fig.5 Transfer Characteristics

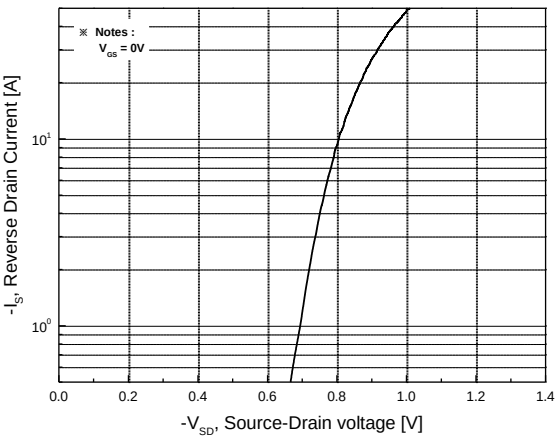


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

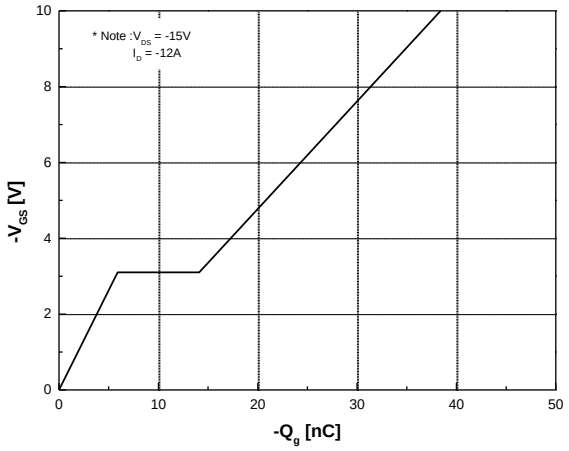


Fig.7 Gate Charge Characteristics

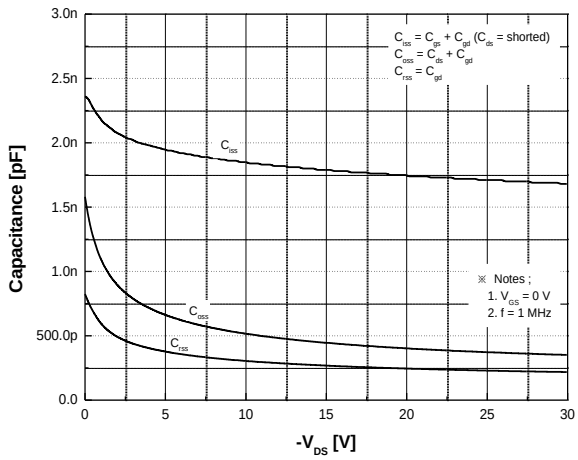


Fig.8 Capacitance Characteristics

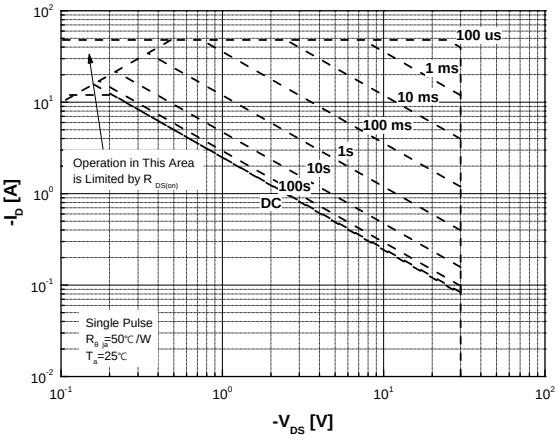


Fig.9 Maximum Safe Operating Area

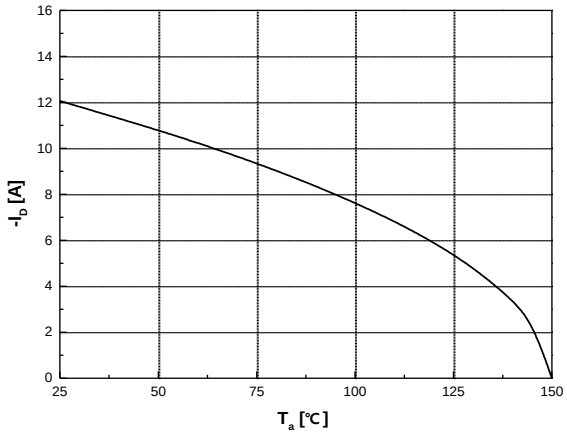


Fig.10 Maximum Drain Current vs. Ambient Temperature

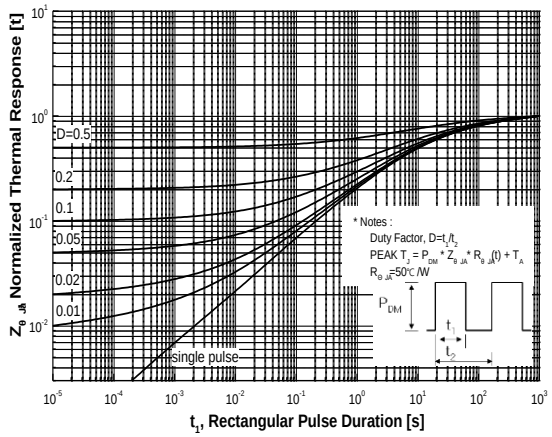
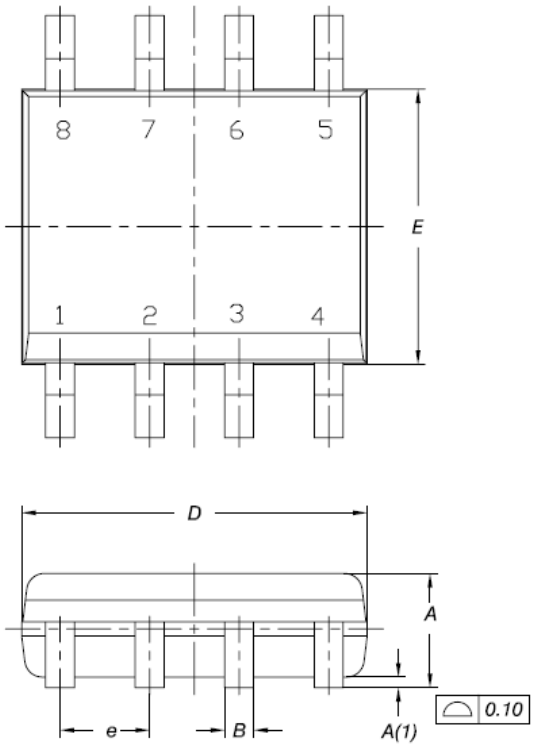


Fig.11 Transient Thermal Response Curve

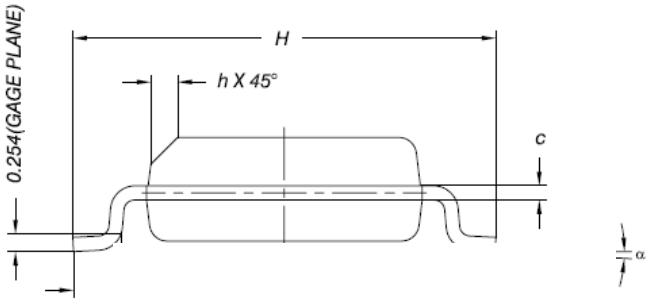
Physical Dimensions

SOIC-8L

Dimensions are in millimeters unless otherwise specified



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	1.35	1.55	1.75
A(1)	0.10	0.175	0.25
B	0.38	0.445	0.51
C	0.19	0.22	0.25
D	4.80	4.90	5.00
E	3.80	3.90	4.00
e	1.27 BSC		
H	5.80	6.00	6.20
L	0.50	0.715	0.93
α	0°	4°	8°
h	0.25	0.375	0.50



Note : Package body size, length and width do not include mold flash, protrusions and gate burrs.

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