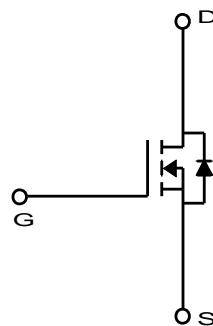
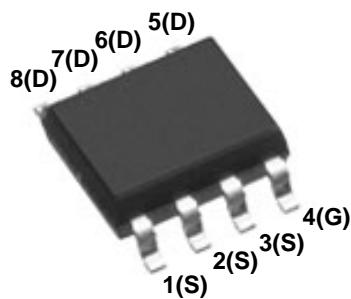


General Description

The MDS1526 uses advanced Magnachip's MOSFET Technology, which provides high performance in on-state resistance, fast switching performance and excellent quality. MDS1526 is suitable for DC/DC converter and general purpose applications.

Features

- $V_{DS} = 30V$
- $I_D = 16.1A @ V_{GS} = 10V$
- $R_{DS(ON)}$
 $< 11.0m\Omega @ V_{GS} = 10V$
 $< 16.4m\Omega @ V_{GS} = 4.5V$
- 100% UIL Tested
- 100% R_g Tested



Absolute Maximum Ratings ($T_a = 25^\circ C$)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	30	V
Gate-Source Voltage		V_{GSS}	± 20	V
Continuous Drain Current ⁽¹⁾	$T_c=25^\circ C$	I_D	16.1	A
	$T_c=70^\circ C$		12.8	
	$T_a=25^\circ C$		11.3 ⁽³⁾	
	$T_a=70^\circ C$		9.1 ⁽³⁾	
Pulsed Drain Current		I_{DM}	40	A
Power Dissipation	$T_c=25^\circ C$	P_D	5.0	W
	$T_c=70^\circ C$		3.2	
	$T_a=25^\circ C$		2.5 ⁽³⁾	
	$T_a=70^\circ C$		1.6 ⁽³⁾	
Single Pulse Avalanche Energy ⁽²⁾		E_{AS}	38	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	°C

Thermal Characteristics

Characteristics		Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient ⁽¹⁾		$R_{\theta JA}$	50	°C/W
Thermal Resistance, Junction-to-Case		$R_{\theta JC}$	24.6	

Ordering Information

Part Number	Temp. Range	Package	Packing	Quantity	Rohs Status
MDS1526URH	-55~150°C	SOIC-8	Tape & Reel	3000 units	Halogen Free

Electrical Characteristics ($T_J = 25^\circ\text{C}$)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	30	-	-	V
Gate Threshold Voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	1.3	1.9	2.7	
Drain Cut-Off Current	I_{DSS}	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$	-	-	1	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$	-	-	± 0.1	
Drain-Source ON Resistance	$R_{DS(\text{ON})}$	$V_{GS} = 10\text{V}, I_D = 9\text{A}$ $T_J = 125^\circ\text{C}$	-	9.6	11.0	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}, I_D = 7\text{A}$	-	13.9	16.0	
Forward Transconductance	g_{fs}	$V_{DS} = 5\text{V}, I_D = 9\text{A}$	-	13.7	16.4	
			-	22	-	S
Dynamic Characteristics						
Total Gate Charge	$Q_{g(10\text{V})}$	$V_{DS} = 15.0\text{V}, I_D = 9\text{A}, V_{GS} = 10\text{V}$	7.8	11.2	14.5	nC
Total Gate Charge	$Q_{g(4.5\text{V})}$		3.8	5.4	7.0	
Gate-Source Charge	Q_{gs}		-	2.0	-	
Gate-Drain Charge	Q_{gd}		-	1.8	-	
Input Capacitance	C_{iss}	$V_{DS} = 15.0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$	485	692	900	pF
Reverse Transfer Capacitance	C_{rss}		47	68	88	
Output Capacitance	C_{oss}		97	139	180	
Turn-On Delay Time	$t_{d(on)}$	$V_{GS} = 10\text{V}, V_{DS} = 15.0\text{V}, I_D = 9\text{A}, R_G = 3.0\Omega$	-	5.9	-	ns
Rise Time	t_r		-	4.0	-	
Turn-Off Delay Time	$t_{d(off)}$		-	18.6	-	
Fall Time	t_f		-	8.6	-	
Gate Resistance	R_g	$f=1\text{ MHz}$	0.5	1.7	3.0	Ω
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V_{SD}	$I_S = 9\text{A}, V_{GS} = 0\text{V}$	-	0.83	1.1	V
Body Diode Reverse Recovery Time	t_{rr}	$I_F = 9\text{A}, dI/dt = 100\text{A}/\mu\text{s}$	-	17.8	26.7	ns
Body Diode Reverse Recovery Charge	Q_{rr}		-	8.9	13.3	nC

Note :

1. Surface mounted FR-4 board by JEDEC (jesd51-7)
2. E_{AS} is tested at starting $T_J = 25^\circ\text{C}$, $L = 0.1\text{mH}$, $I_{AS} = 14.8\text{A}$, $V_{DD} = 27\text{V}$, $V_{GS} = 10\text{V}$.
3. $T < 10\text{sec}$

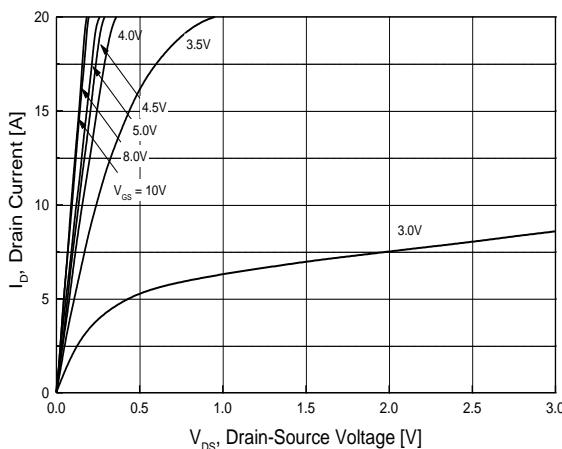


Fig.1 On-Region Characteristics

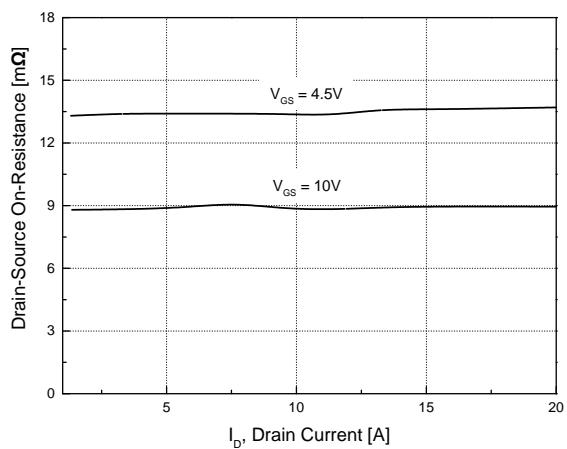


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

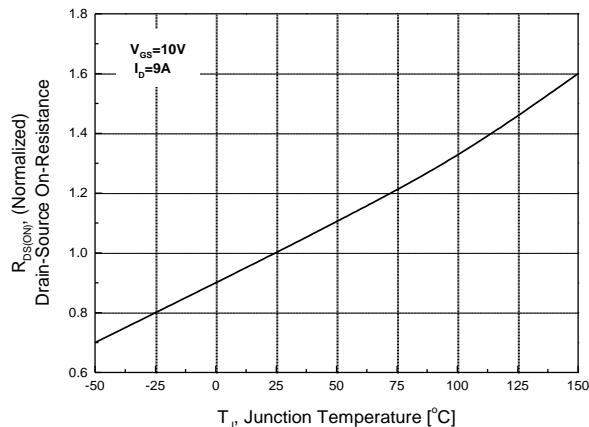


Fig.3 On-Resistance Variation with Temperature

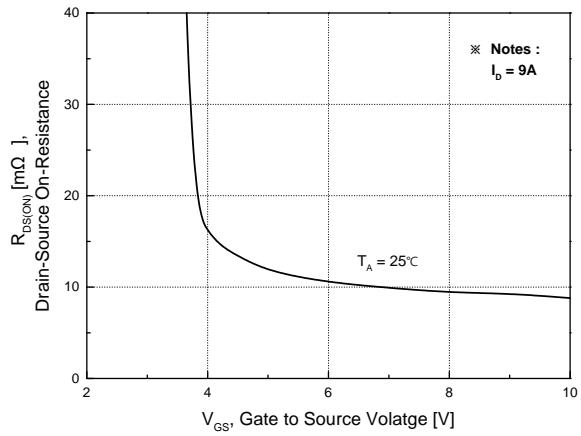


Fig.4 On-Resistance Variation with Gate to Source Voltage

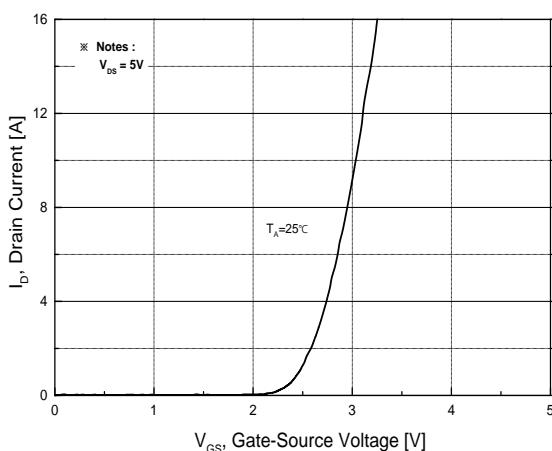


Fig.5 Transfer Characteristics

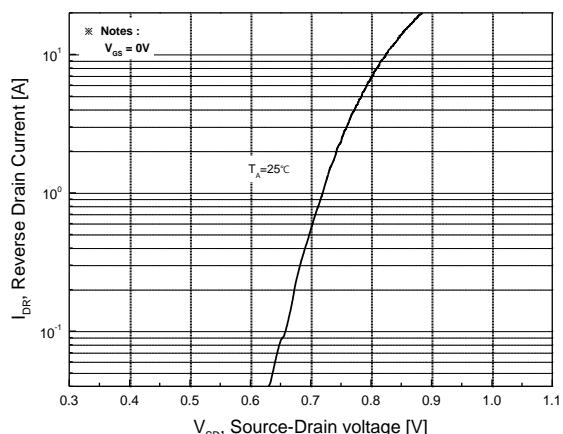


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

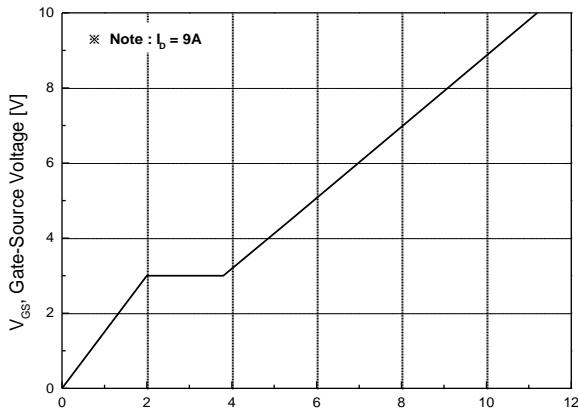


Fig.7 Gate Charge Characteristics

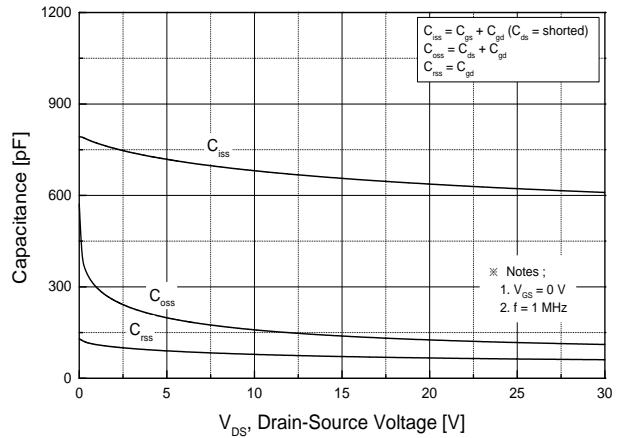


Fig.8 Capacitance Characteristics

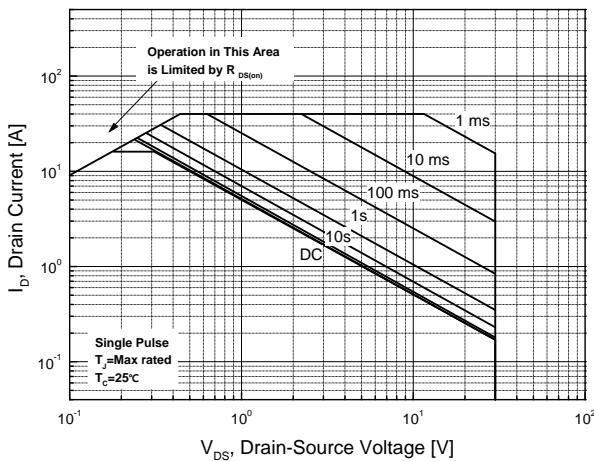


Fig.9 Maximum Safe Operating Area

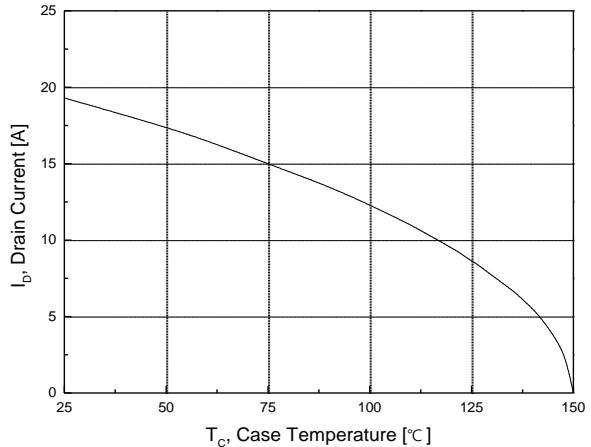


Fig.10 Maximum Drain Current vs. Case Temperature

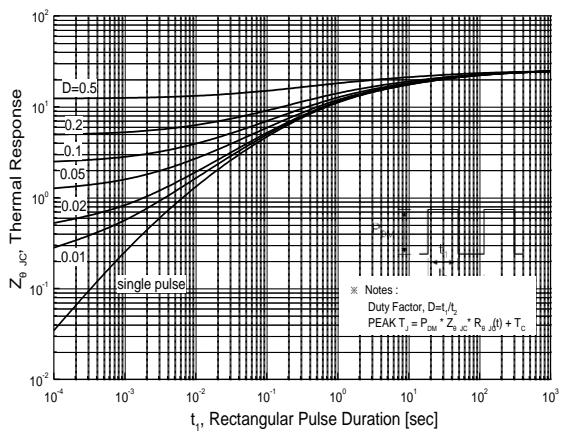
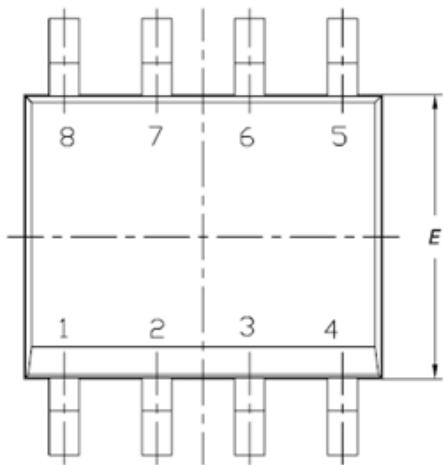


Fig.11 Transient Thermal Response Curve

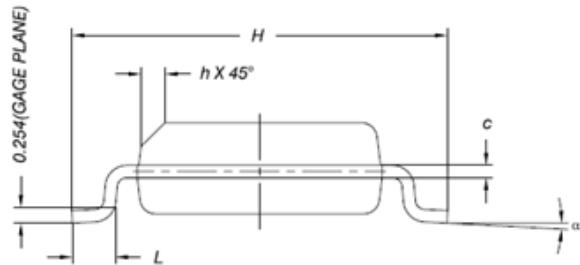
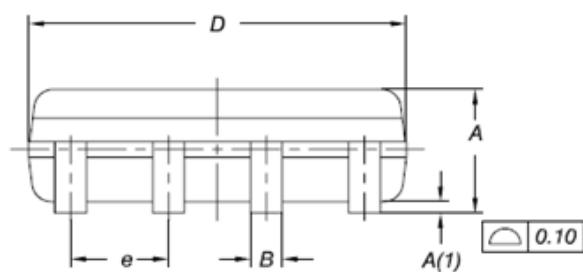
Physical Dimensions

8 Leads, SOIC

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	-	-	1.75
A(1)	0.10	-	0.25
B	0.31	-	0.51
C	0.10	-	0.25
D	4.9 BSC		
E	3.9 BSC		
e	1.27 BSC		
H	6.0 BSC		
L	0.40	-	1.27
a	0	-	8
h	0.250	-	0.500
L2(Gage plane)	0.25 BSC		



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