



# MDF11N65B

## N-Channel MOSFET 650V, 12A, 0.65Ω

MDF11N65B N-channel MOSFET 650V

### General Description

These N-channel MOSFET are produced using advanced Magnachip's MOSFET Technology, which provides low on-state resistance, high switching performance and excellent quality.

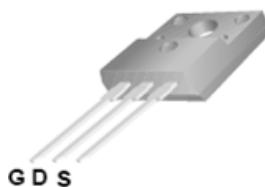
These devices are suitable device for SMPS, high Speed switching and general purpose applications.

### Features

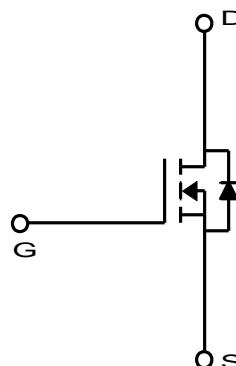
- $V_{DS} = 650V$
- $I_D = 12A$  @  $V_{GS} = 10V$
- $R_{DS(ON)} \leq 0.65\Omega$  @  $V_{GS} = 10V$

### Applications

- Power Supply
- PFC
- High Current, High Speed Switching



TO-220F  
MDF Series



### Absolute Maximum Ratings ( $T_a = 25^\circ C$ )

Characteristics	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DSS}$	650	V
Gate-Source Voltage	$V_{GSS}$	$\pm 30$	V
Continuous Drain Current	$I_D$	12*	A
$T_c=100^\circ C$		7.7*	A
Pulsed Drain Current <sup>(1)</sup>	$I_{DM}$	48*	A
Power Dissipation	$P_D$	49.6	W
$T_c=25^\circ C$		0.4	W/ $^\circ C$
Repetitive Avalanche Energy <sup>(1)</sup>	$E_{AR}$	18.1	mJ
Peak Diode Recovery $dv/dt^{(3)}$	$dv/dt$	4.5	V/ns
Single Pulse Avalanche Energy <sup>(4)</sup>	$E_{AS}$	750	mJ
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~150	$^\circ C$

\*  $I_d$  limited by maximum junction temperature

### Thermal Characteristics

Characteristics	Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient <sup>(1)</sup>	$R_{\theta JA}$	62.5	$^\circ C/W$
Thermal Resistance, Junction-to-Case <sup>(1)</sup>	$R_{\theta JC}$	2.52	

## Ordering Information

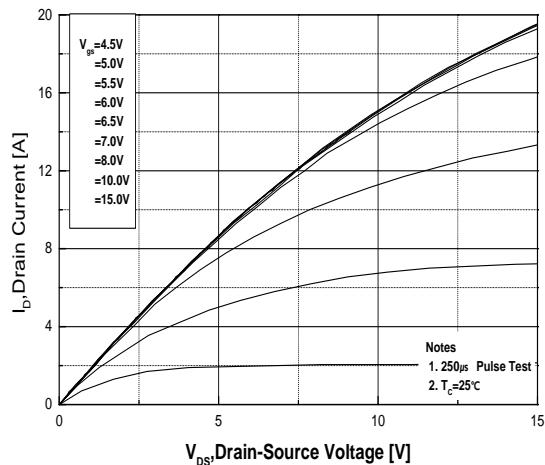
Part Number	Temp. Range	Package	Packing	RoHS Status
MDF11N65BTH	-55~150°C	TO-220F	Tube	Halogen Free

## Electrical Characteristics (Ta =25°C)

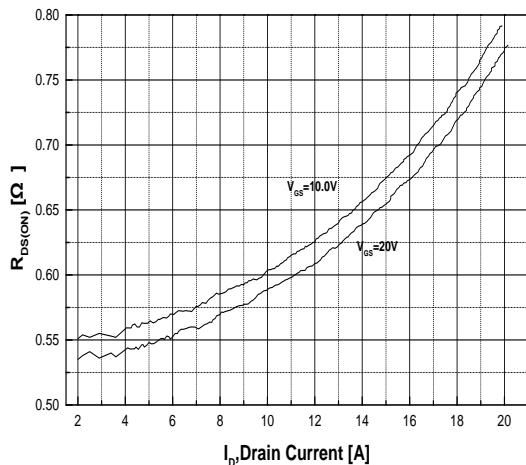
Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	I <sub>D</sub> = 250μA, V <sub>GS</sub> = 0V	650	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA	2.0	-	4.0	
Drain Cut-Off Current	I <sub>DSS</sub>	V <sub>DS</sub> = 650V, V <sub>GS</sub> = 0V	-	-	1	μA
Gate Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> = ±30V, V <sub>DS</sub> = 0V	-	-	100	nA
Drain-Source ON Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A		0.55	0.65	Ω
Forward Transconductance	g <sub>f</sub>	V <sub>DS</sub> = 30V, I <sub>D</sub> = 6A	-	8.7	-	S
<b>Dynamic Characteristics</b>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> = 520V, I <sub>D</sub> = 12.0A, V <sub>GS</sub> = 10V <sup>(3)</sup>	-	31	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	9.2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	12.6	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 25V, V <sub>GS</sub> = 0V, f = 1.0MHz	-	1650	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	7.7	-	
Output Capacitance	C <sub>oss</sub>		-	180	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 325V, I <sub>D</sub> = 12.0A, R <sub>G</sub> = 25Ω <sup>(3)</sup>	-	27	-	ns
Rise Time	t <sub>r</sub>		-	52	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	132	-	
Fall Time	t <sub>f</sub>		-	48	-	
<b>Drain-Source Body Diode Characteristics</b>						
Maximum Continuous Drain to Source Diode Forward Current	I <sub>S</sub>	I <sub>F</sub> = 12.0A, di/dt = 100A/μs	-	12	-	A
Source-Drain Diode Forward Voltage	V <sub>SD</sub>		-	-	1.4	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	355	-	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>		-	3.6	-	μC

Note :

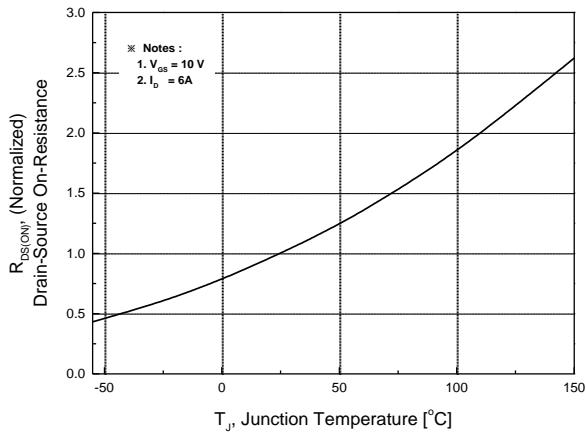
1. Pulse width is based on R<sub>ESC</sub> & R<sub>SDA</sub> and the maximum allowed junction temperature of 150°C.
2. Pulse test: pulse width ≤300us, duty cycle≤2%, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C.
3. I<sub>SD</sub> ≤12.0A, di/dt≤200A/us, V<sub>DD</sub>≤BV<sub>DSS</sub>, R<sub>g</sub> =25Ω, Starting T<sub>J</sub>=25°C
4. L=9.62mH, I<sub>AS</sub>=12.0A, V<sub>DD</sub>=50V, R<sub>g</sub> =25Ω, Starting T<sub>J</sub>=25°C,



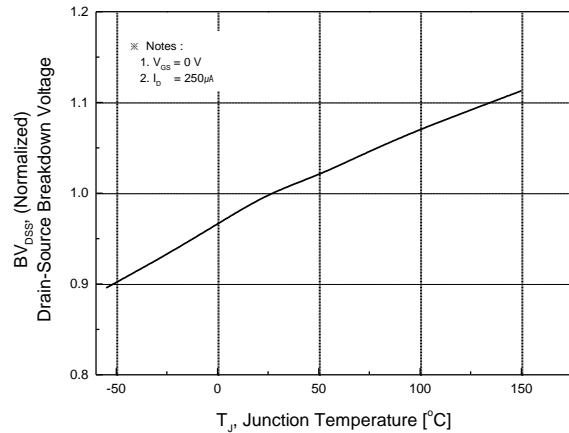
**Fig.1 On-Region Characteristics**



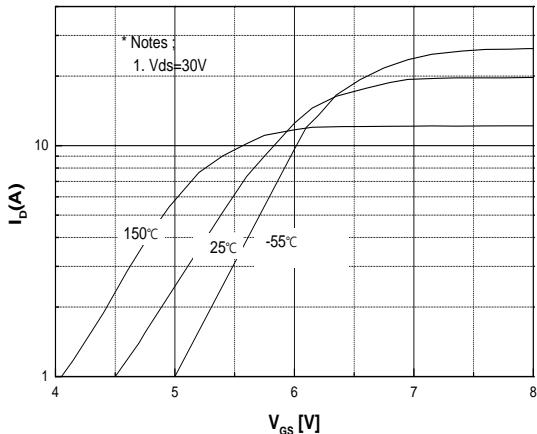
**Fig.2 On-Resistance Variation with Drain Current and Gate Voltage**



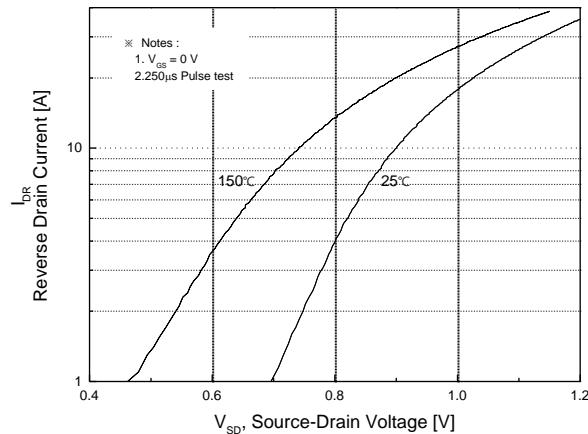
**Fig.3 On-Resistance Variation with Temperature**



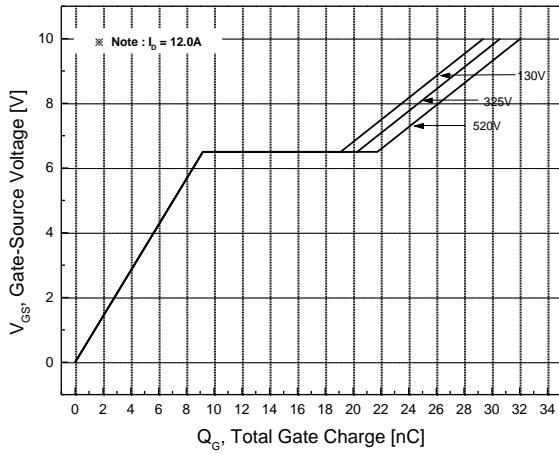
**Fig.4 Breakdown Voltage Variation vs. Temperature**



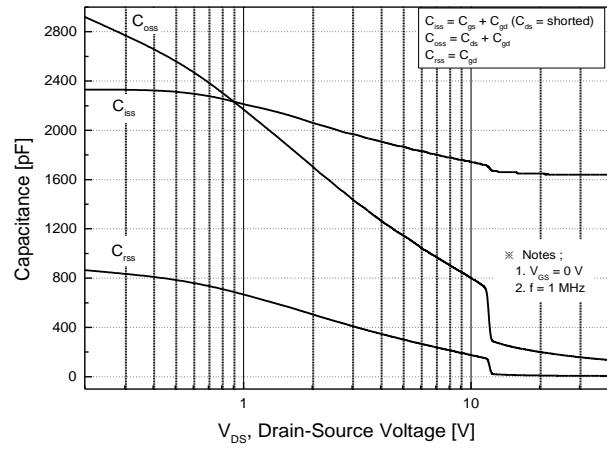
**Fig.5 Transfer Characteristics**



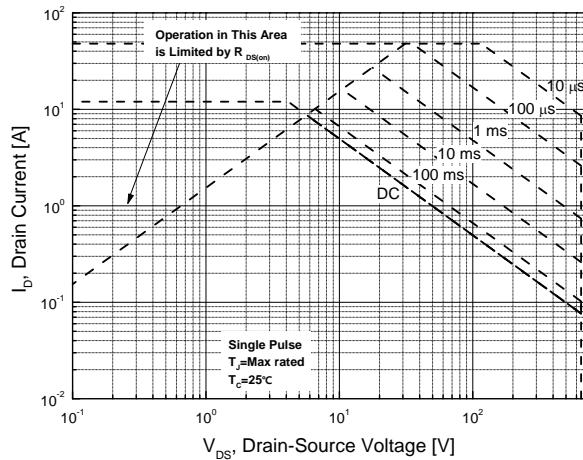
**Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature**



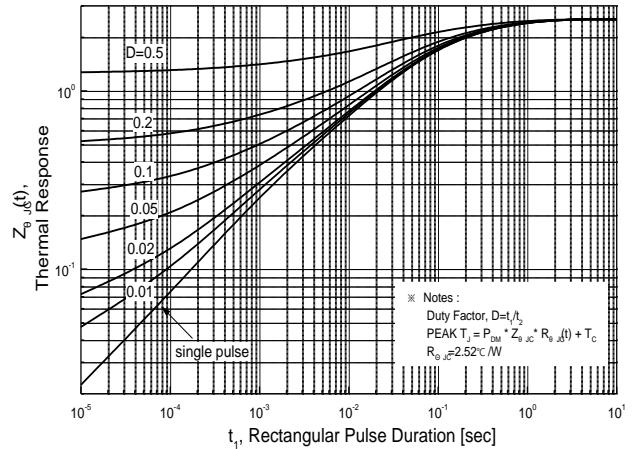
**Fig.7 Gate Charge Characteristics**



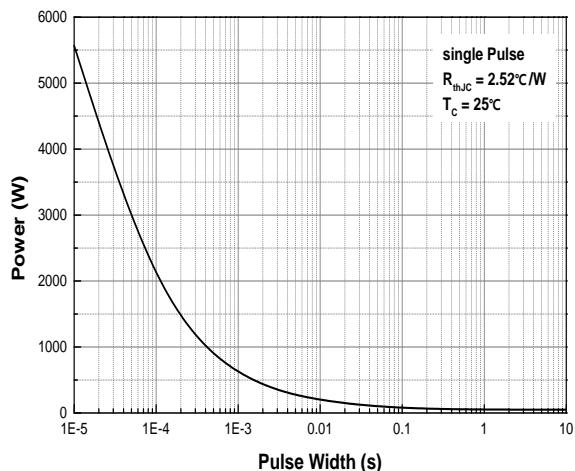
**Fig.8 Capacitance Characteristics**



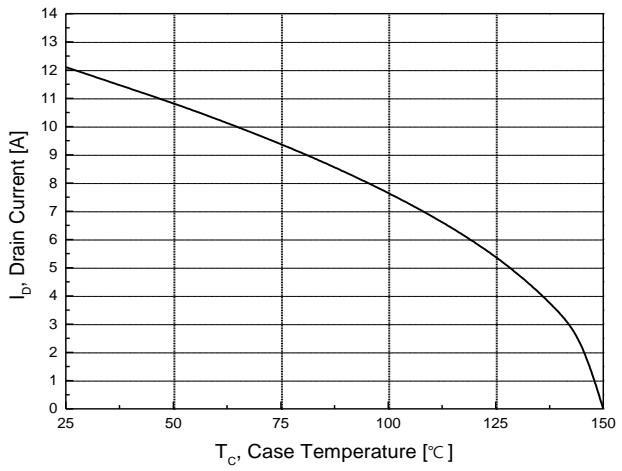
**Fig.9 Maximum Safe Operating Area**



**Fig.10 Transient Thermal Response Curve**



**Fig.11 Single Pulse Maximum Power Dissipation**

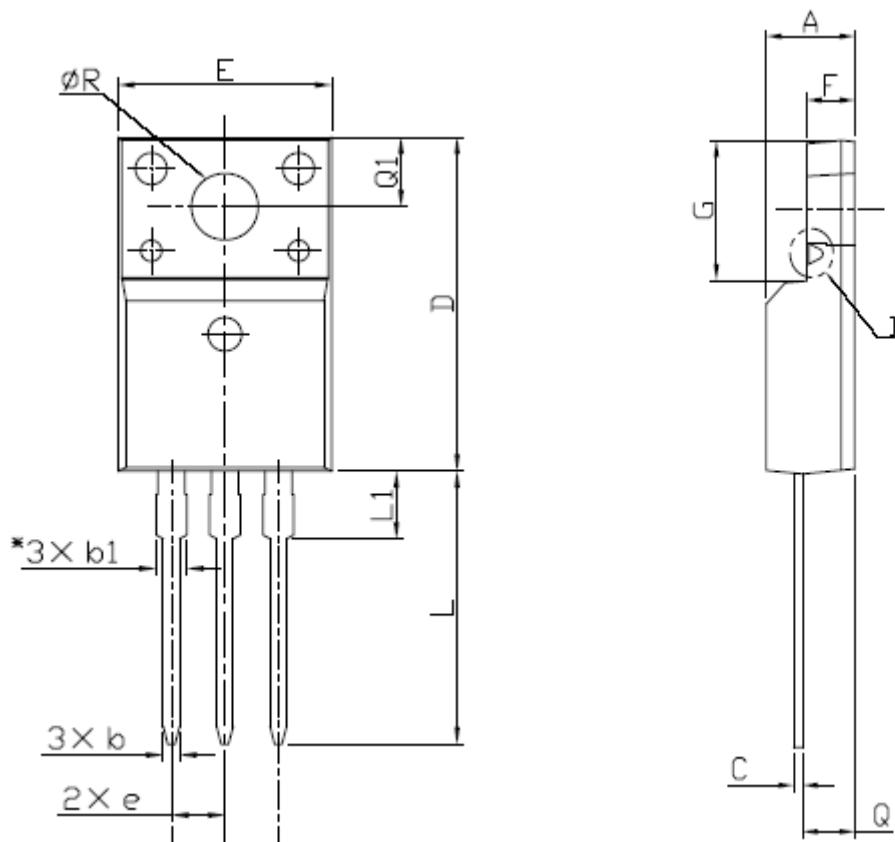


**Fig.12 Maximum Drain Current vs. Case Temperature**

## Physical Dimensions

### 3 Leads, TO-220F

Dimensions are in millimeters unless otherwise specified



Symbol	Min	Nom	Max
A	4.50		4.93
b	0.63		0.91
b1	1.15		1.47
C	0.33		0.63
D	15.47		16.13
E	9.60		10.71
e		2.54	
F	2.34		2.84
G	6.48		6.90
L	12.24		13.72
L1	2.79		3.67
Q	2.52		2.96
Q1	3.10		3.50
ØR	3.00		3.55

Note : Package body size, length and width do not include mold flash, protrusions and gate burrs.

**DISCLAIMER:**

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