

# MAP9004E/D

High Voltage AC LED Driver

#### **General Description**

The MAP9004E/D is LED Driver which has high input voltage ranged from 90V to 270V.

It can drive several series LEDs from rectified AC voltage.

The MAP9004E/D has higher LED current drive capability up to 240mA and the current can be adjustable with external resistors.

The MAP9004E/D is available in eSOP-8 and DFN-33 4LD with Halogen-free (fully RoHS compliant).

For more information, please contact local Magnachip sales office in world-wide or visit Magnachip's website

#### RoHS Compliant

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**MARCH 2017** 

#### Features

- Wide operating voltage range
- Higher current drive capability
- Multiple connection for better efficiency, PF & THD
- EMI improvement
- OTP protection
- eSOP-8/DFN-33 4LD package

#### **Applications**

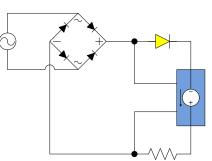
- AC LED Driver
- Lighting equipment
- LED Driver Power Supplies

## **Ordering Information**

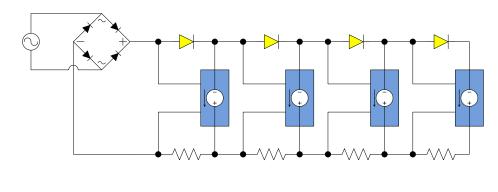
Part Number	Top Marking	Ambient Temperature Range	Package	RoHS Status
MAP9004ESRH	MAP9004E	-30 ℃ to +85 ℃	eSOP-8	Halogen Free
MAP9004DFRH	MAP9004D	-30 ℃ to +85 ℃	DFN-33 4LD	Halogen Free

#### **Simplified Application Circuit**

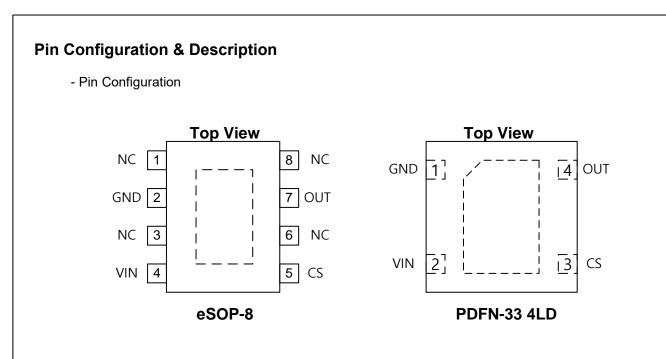
- Single Stage



- Multi Stages for Better Efficiency, PF & THD

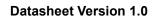




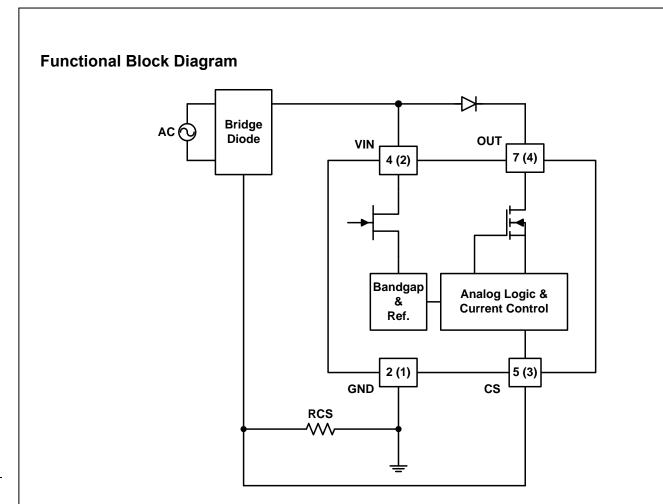


## - Pin Description

Pin (PDFN-33)		Descriptions	
2 (1)	GND	Ground	
4 (2)	VIN	Voltage input	
5 (3)	CS	Current sensing	
7 (4)	OUT	Output	



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# Absolute Maximum Ratings

PARAMETER		VALUE	UNIT
VIN		500	V
OUT		-0.3 ~ 500	V
CS		-6V ~ 0.3	V
Operating Temperature Range		-40 ~ 125	Ĵ
Junction Temperature Range		-40 ~ 150	Ĵ
Storage Temperature Range		-65 ~ 150	Ĵ
Lead temperature(soldering, 10sec)		260	Ĵ
	HBM (Note 1)	4000	V
ESD Susceptibility	MM (Note 2)	400	V
	CDM (Note 3)	2000	V

Note 1: ESD tested per JESD22A-114. Note 2: ESD tested per JESD22A-115. Note 3: ESD tested per JESD22C-101E

## **Thermal Resistance**

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PARAMETER		VALUE	UNIT
Thermal Desistence (0, ) (Note 1)	eSOP-8	71.2	°C∕W
Thermal Resistance $(\theta_{JA})$ , (Note4)	DFN-33 4LD	65.9	
Thermal Resistance $(\theta_{JT})$ , (Note5)	eSOP-8	26.1	
	DFN-33 4LD	23.1	

**Note 4**: Multi-layer PCB based on JEDEC standard (JESD51-7, 4Layer PCB)

**Note 5**: The metal PCB's diameter is 43mm and height is 1.6t

## **Datasheet Version 1.0**

# **Electrical Characteristics**

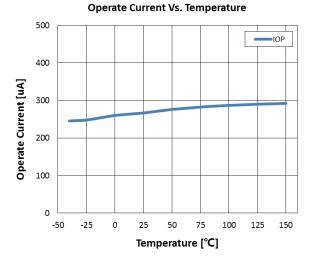
Ta = 25  $^\circ \!\! C$  , CS Resistance = 6.8  $\!\Omega$  unless otherwise specified

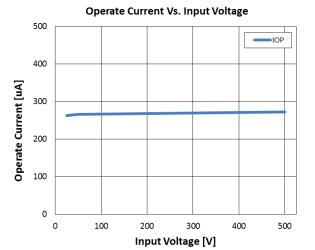
SYMBOL	PARAMETER	TEST CONDITION	MIN	ТҮР	MAX	UNIT
Supply						
Vvin_min	Minimum Startup Voltage				25	V
I∨in	Input current	VVIN = 200V, VGND = 0V	150	350	700	uA
Driver Section						
ID Leak	Driver leakage current	$VVIN = 0V, VGND = 0V, \\ OUT = 400V$	-	-	10	uA
Ιουτ	Driver current	VVIN =200V, OUT = 40V	180	185	190	mA
OTP Section						
OTP	Over temperature protection		150	-	-	Ĵ

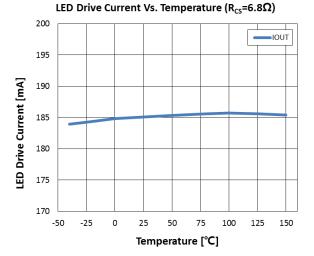
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**Note 6**: Stress beyond the maximum ratings listed above may incur permanent damage to the device. Operating above the recommended conditions for extended time may stress the device and affect device reliability. Also the device may not operate normally above the recommended operating conditions. These are stress ratings only.

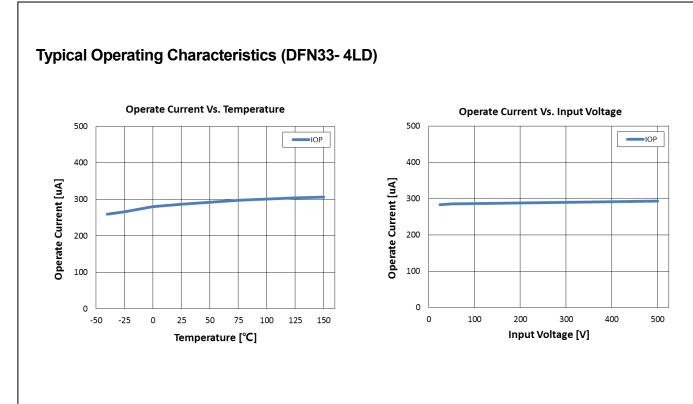
# **Typical Operating Characteristics (eSOP-8)**

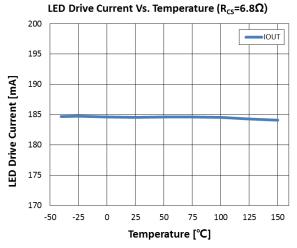






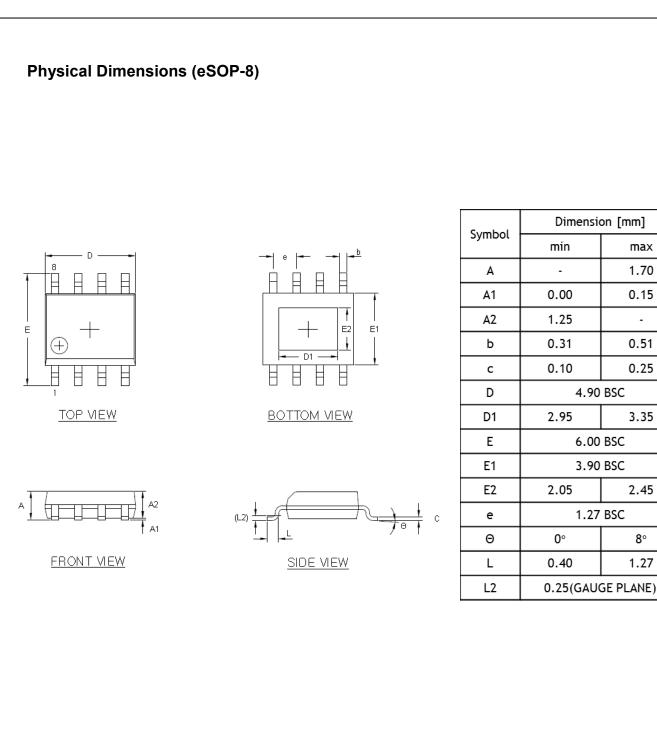
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