

General Description

The MDU3605 uses advanced MagnaChip's MOSFET Technology to provide low on-state resistance.

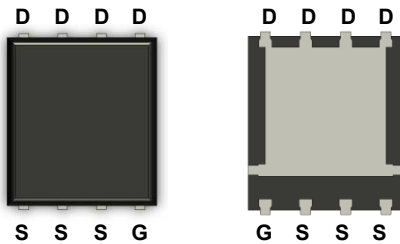
This device is suited for Power Management and load switching applications common in Notebook Computers and Portable Battery Packs.

Features

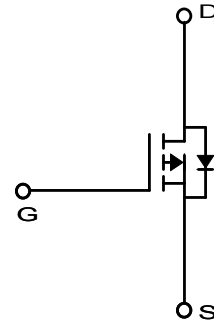
- $V_{DS} = -30V$
- $I_D = -35.6A$ @ $V_{GS} = -10V$
- $R_{DS(ON)} < 17.0m\Omega$ @ $V_{GS} = -10V$
- $< 27.0m\Omega$ @ $V_{GS} = -5V$

Applications

- Load Switch
- General purpose applications
- Smart Module for Note PC Battery



PowerDFN56



Absolute Maximum Ratings ($T_a = 25^\circ C$ unless otherwise noted)

Characteristics		Symbol	Rating	Unit
Drain-Source Voltage		V_{DSS}	-30	V
Gate-Source Voltage		V_{GSS}	± 25	V
Continuous Drain Current ⁽¹⁾	$T_c = 25^\circ C$ (Silicon limited)	I_D	-35.6	A
	$T_A = 25^\circ C$		-9.4	
Pulsed Drain Current		I_{DM}	-80.0	A
Power Dissipation	$T_c = 25^\circ C$	P_D	35.7	W
	$T_A = 25^\circ C$		2.5	
Single Pulse Avalanche Energy ⁽²⁾		E_{AS}	78.1	mJ
Junction and Storage Temperature Range		T_J, T_{stg}	-55~150	$^\circ C$

Thermal Characteristics

Characteristics		Symbol	Rating	Unit
Thermal Resistance, Junction-to-Ambient	(Note 1)	$R_{\theta JA}$	50	$^\circ C/W$
Thermal Resistance, Junction-to-Case		$R_{\theta JC}$	3.5	

Ordering Information

Part Number	Temp. Range	Package	Packing	RoHS Status
MDU3605RH	-55~150°C	PowerDFN56	Tape & Reel	Halogen Free

Electrical Characteristics (T_a = 25°C unless otherwise noted)

Characteristics	Symbol	Test Condition	Min	Typ	Max	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	I _D = -250μA, V _{GS} = 0V	-30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250μA	-1.0	-2.0	-3.0	
Drain Cut-Off Current	I _{DSS}	V _{DS} = -30V, V _{GS} = 0V	-		-1	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±25V, V _{DS} = 0V	-	-	±0.1	
Drain-Source ON Resistance	R _{DS(ON)}	V _{GS} = -10V, I _D = -8A	-	13.5	17.0	mΩ
		V _{GS} = -5V, I _D = -8A		20.0	27.0	
Forward Transconductance	g _{FS}	V _{DS} = -5V, I _D = -8A		21.5	-	S
Dynamic Characteristics						
Total Gate Charge	Q _g	V _{DS} = -15V, I _D = -8A V _{GS} = -10V	-	22.0	-	nC
Gate-Source Charge	Q _{gs}		-	3.3	-	
Gate-Drain Charge	Q _{gd}		-	4.3	-	
Input Capacitance	C _{iss}	V _{DS} = -15V, V _{GS} = 0V, f = 1.0MHz	-	1035	-	pF
Reverse Transfer Capacitance	C _{rss}		-	150	-	
Output Capacitance	C _{oss}		-	260	-	
Gate Resistance	R _g	f = 1.0MHz	-	6.4	-	Ω
Turn-On Delay Time	t _{d(on)}	V _{GS} = -10V, V _{DS} = -15V, I _D = -8A, R _{GEN} = 3Ω	-	12.0	-	ns
Turn-On Rise Time	t _r		-	12.4	-	
Turn-Off Delay Time	t _{d(off)}		-	52.1	-	
Turn-Off Fall Time	t _f		-	8.9	-	
Drain-Source Body Diode Characteristics						
Source-Drain Diode Forward Voltage	V _{SD}	I _S = -1A, V _{GS} = 0V	-	-0.71	-1.0	V
Body Diode Reverse Recovery Time	t _{rr}	I _F = -8A, di/dt = 100A/μs	-	30.8		ns
Body Diode Reverse Recovery Charge	Q _{rr}		-	26.4	-	nC

Note :

1. Surface mounted RF4 board with 2oz. Copper.
2. Starting T_J=25°C, L=1.0mH, I_{AS}= -11.0A V_{DD}=-20.0V, V_{GS}=-10.0V. Tested at I_{AS}=-8.5A.

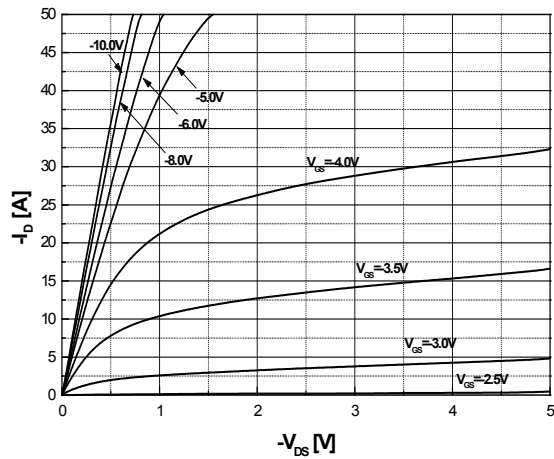


Fig.1 On-Region Characteristics

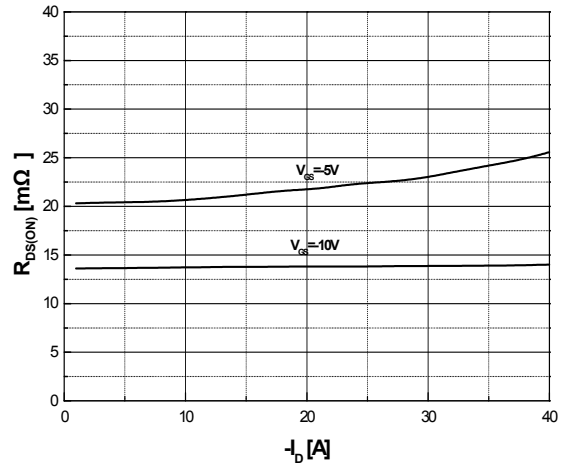


Fig.2 On-Resistance Variation with Drain Current and Gate Voltage

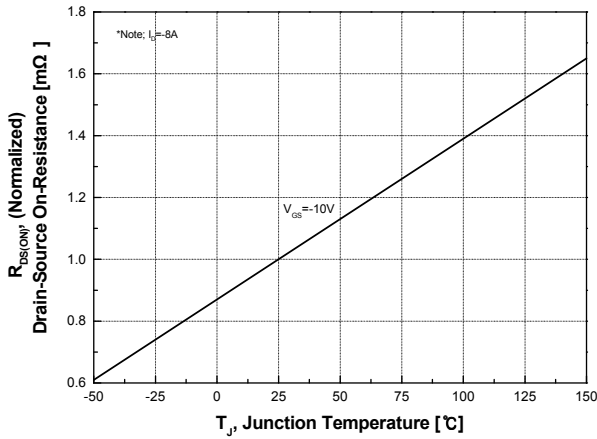


Fig.3 On-Resistance Variation with Temperature

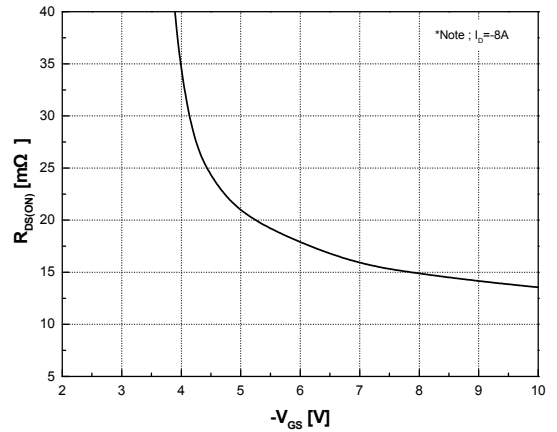


Fig.4 On-Resistance Variation with Gate to Source Voltage

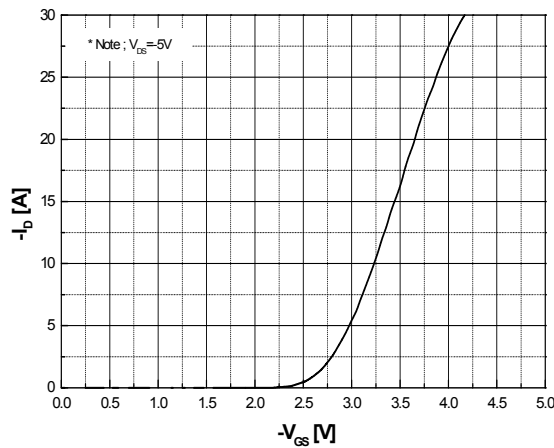


Fig.5 Transfer Characteristics

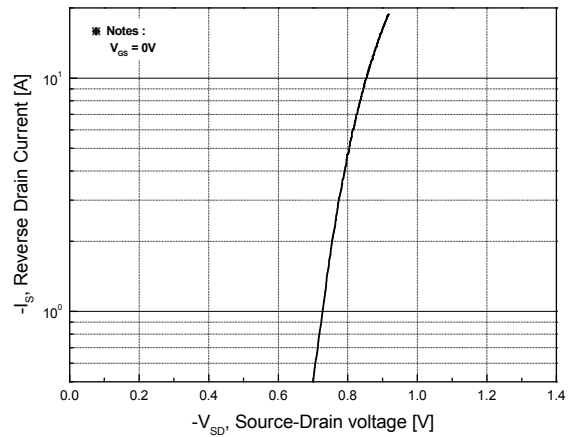


Fig.6 Body Diode Forward Voltage Variation with Source Current and Temperature

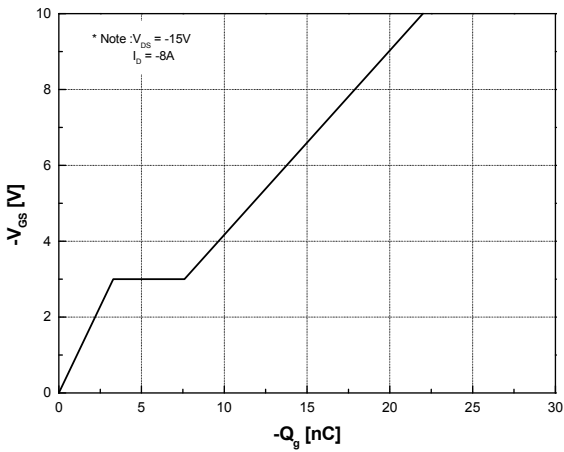


Fig.7 Gate Charge Characteristics

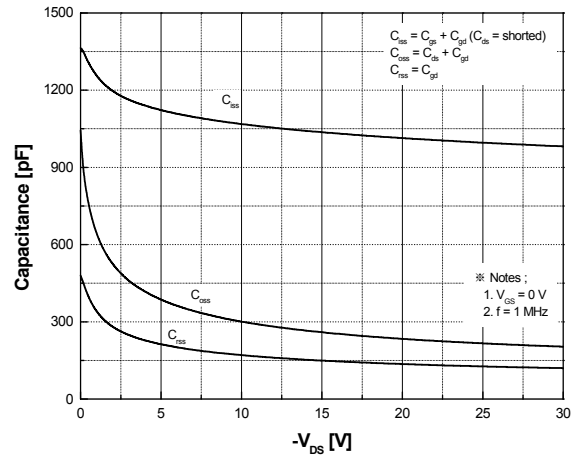


Fig.8 Capacitance Characteristics

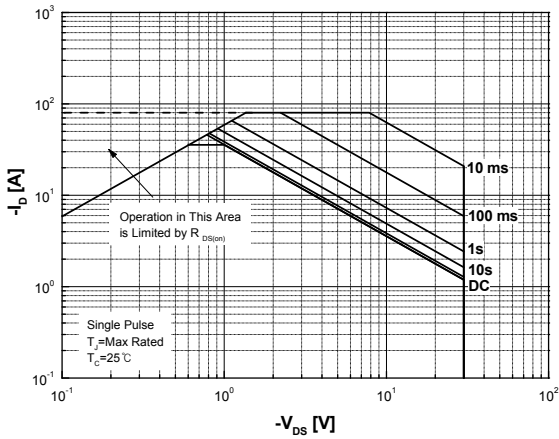


Fig.9 Maximum Safe Operating Area

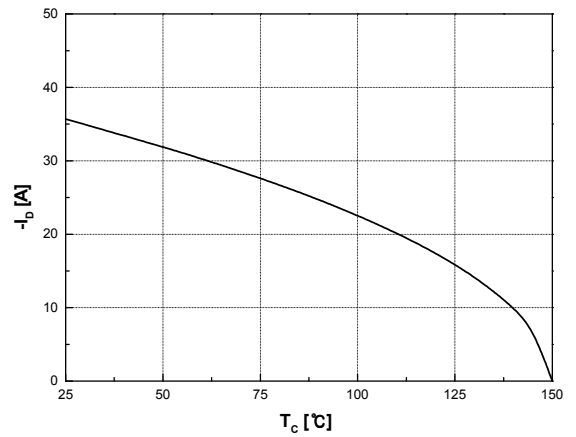


Fig.10 Maximum Drain Current vs. Ambient Temperature

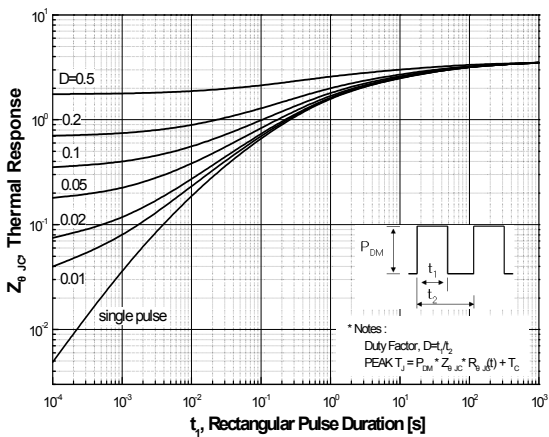
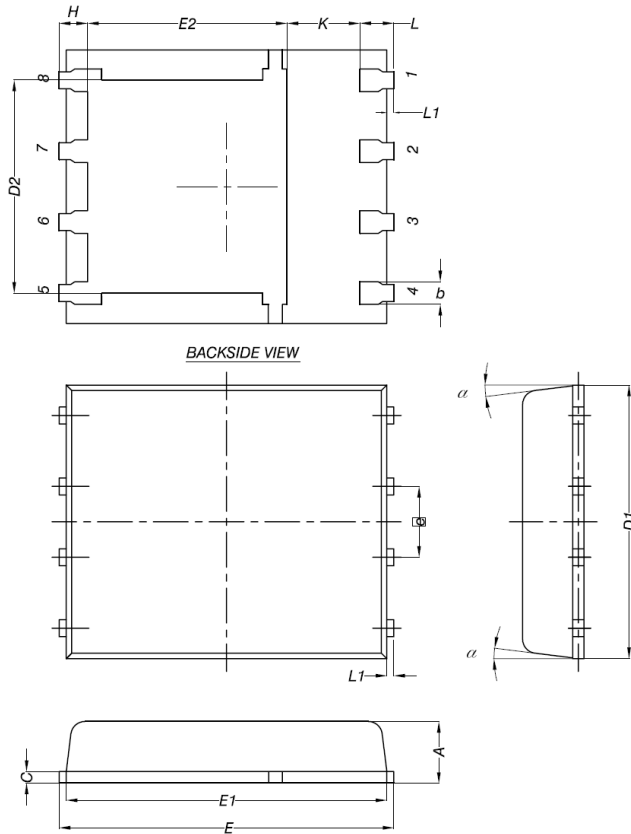


Fig.11 Transient Thermal Response Curve

Package Dimension

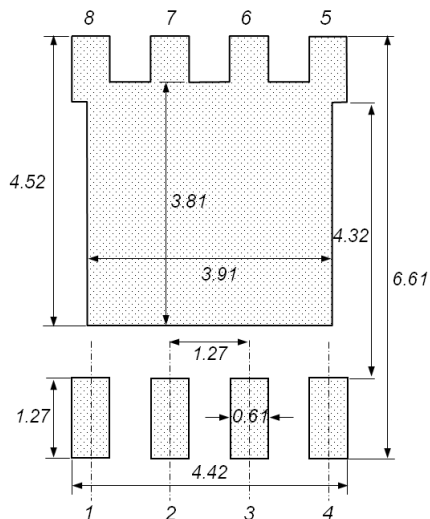
PowerDFN56 (5x6mm)

Dimensions are in millimeters, unless otherwise specified



Dimension	MILLIMETERS	
	Min	Max
A	0.90	1.10
b	0.33	0.51
C	0.20	0.34
D1	4.50	5.10
D2	-	4.22
E	5.90	6.30
E1	5.50	6.10
E2	-	4.30
e	1.27BSC	
H	0.41	0.71
K	0.20	-
L	0.51	0.71
α	0°	12°

Land Pattern



DISCLAIMER:

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